

FIG. 1

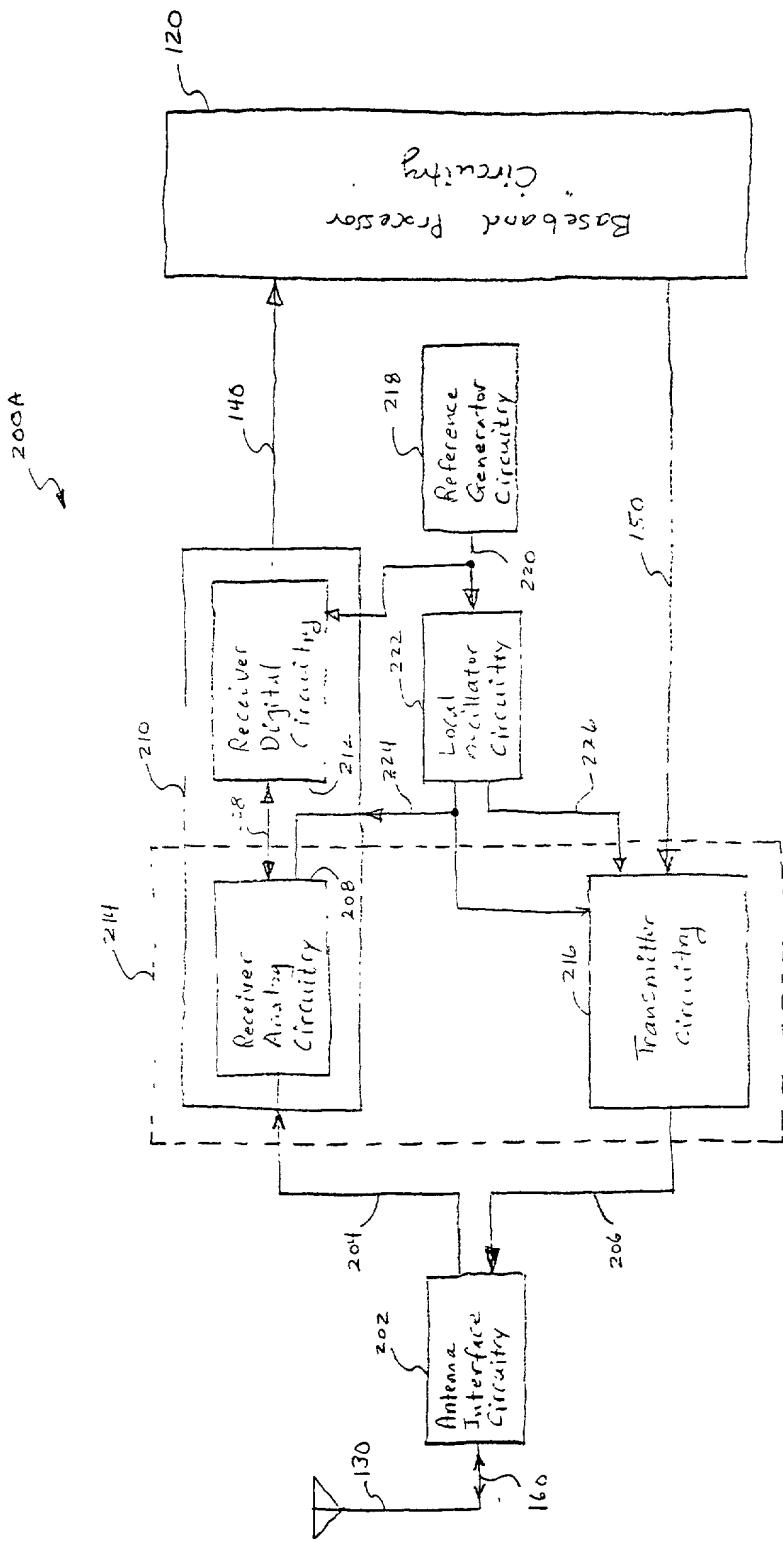


FIG. 2A

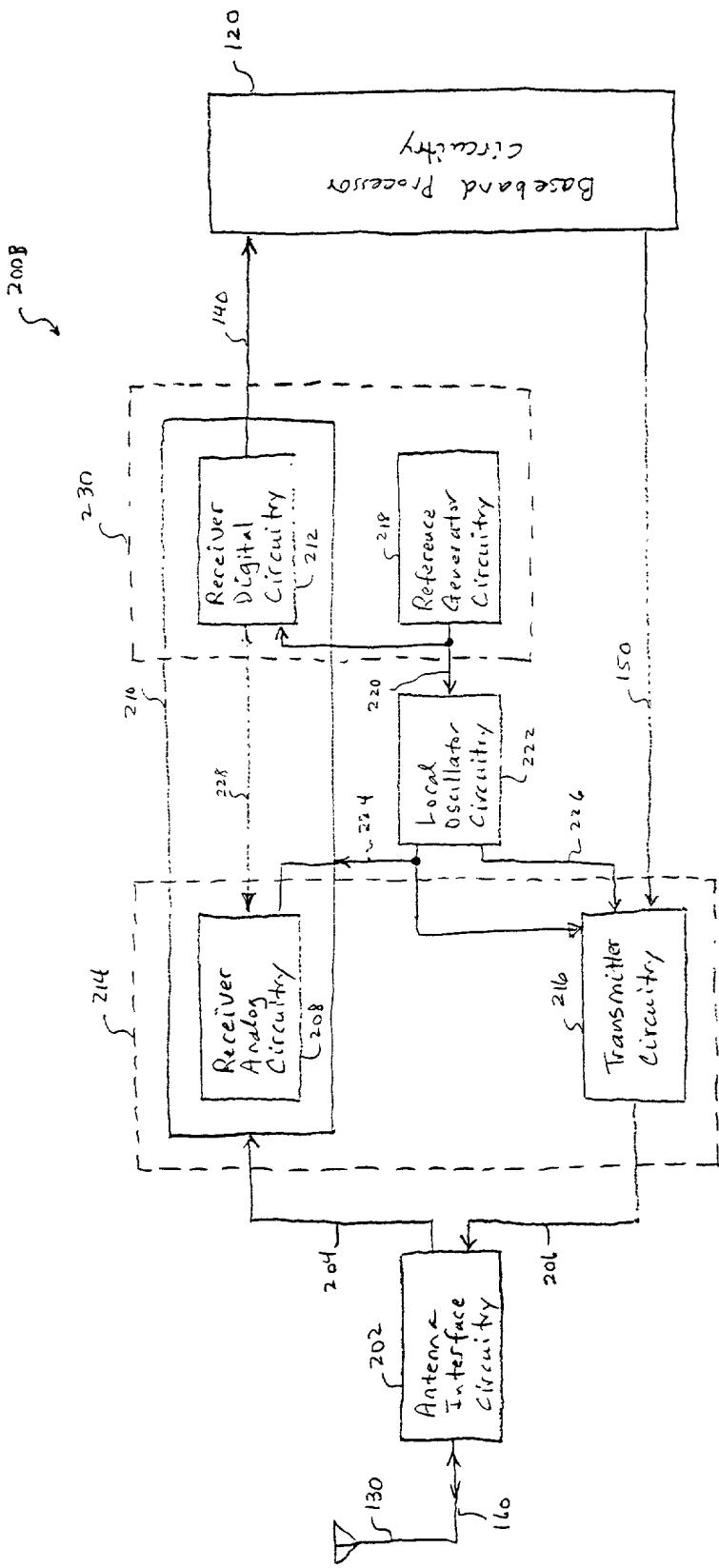


FIG. 2B

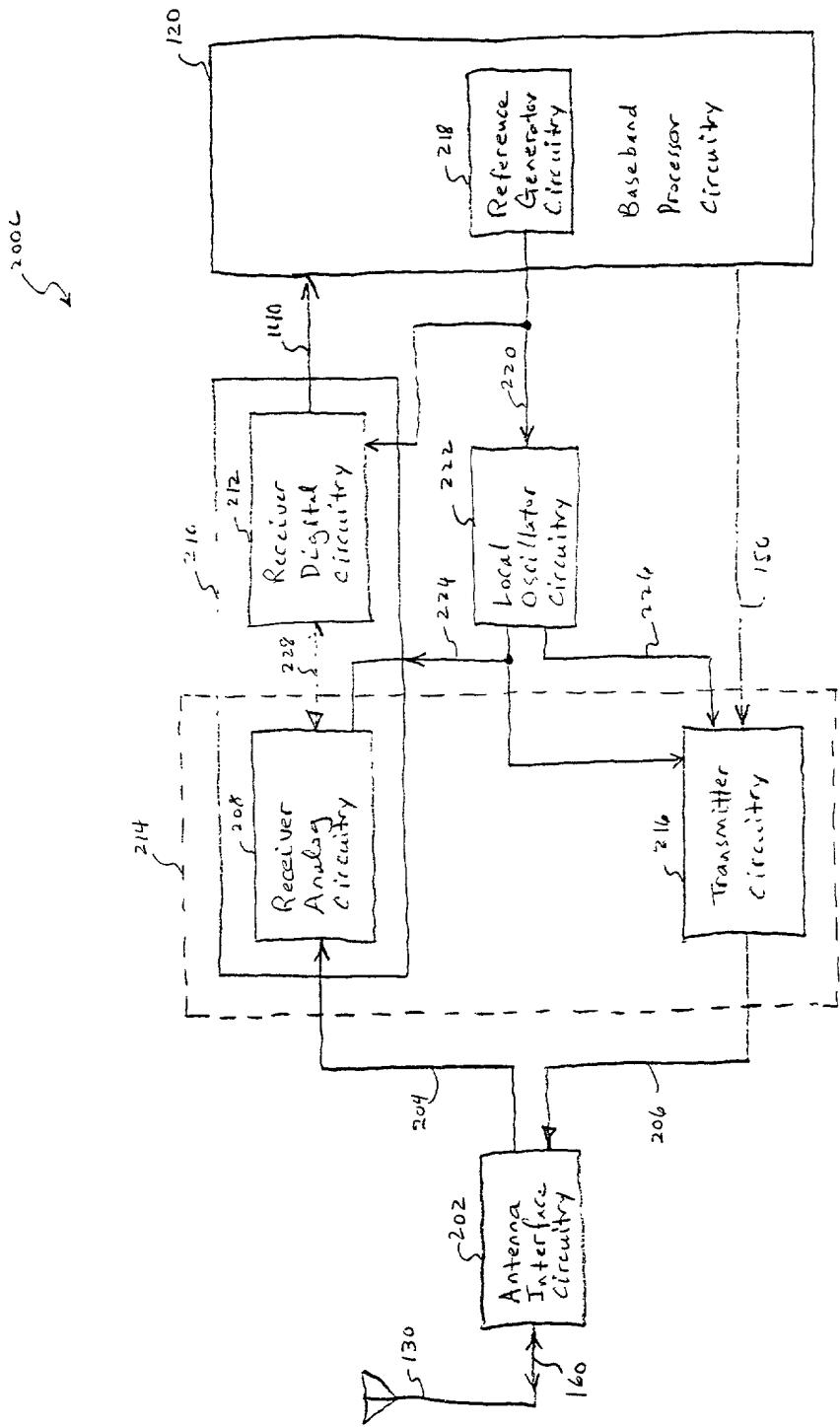
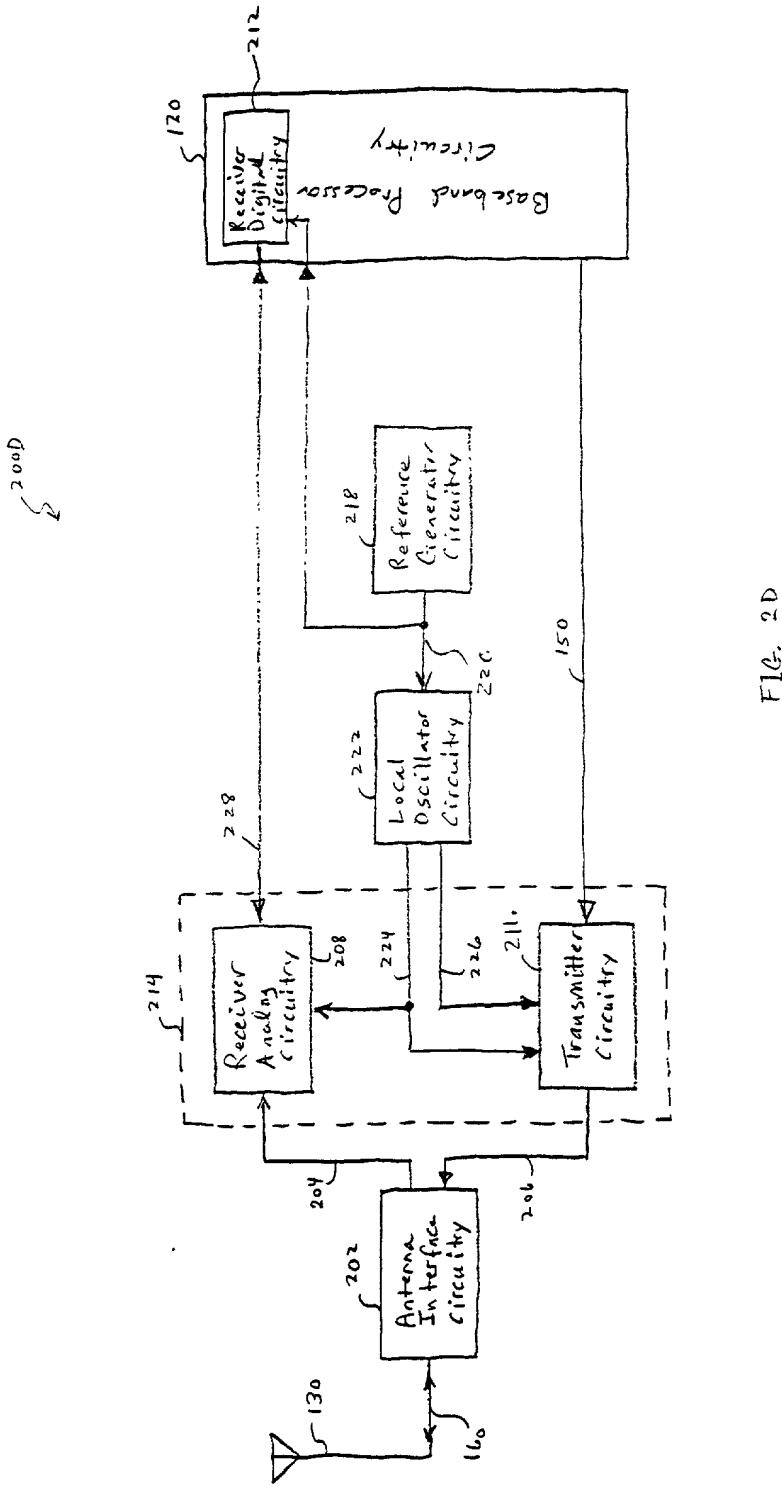


FIG. 2C



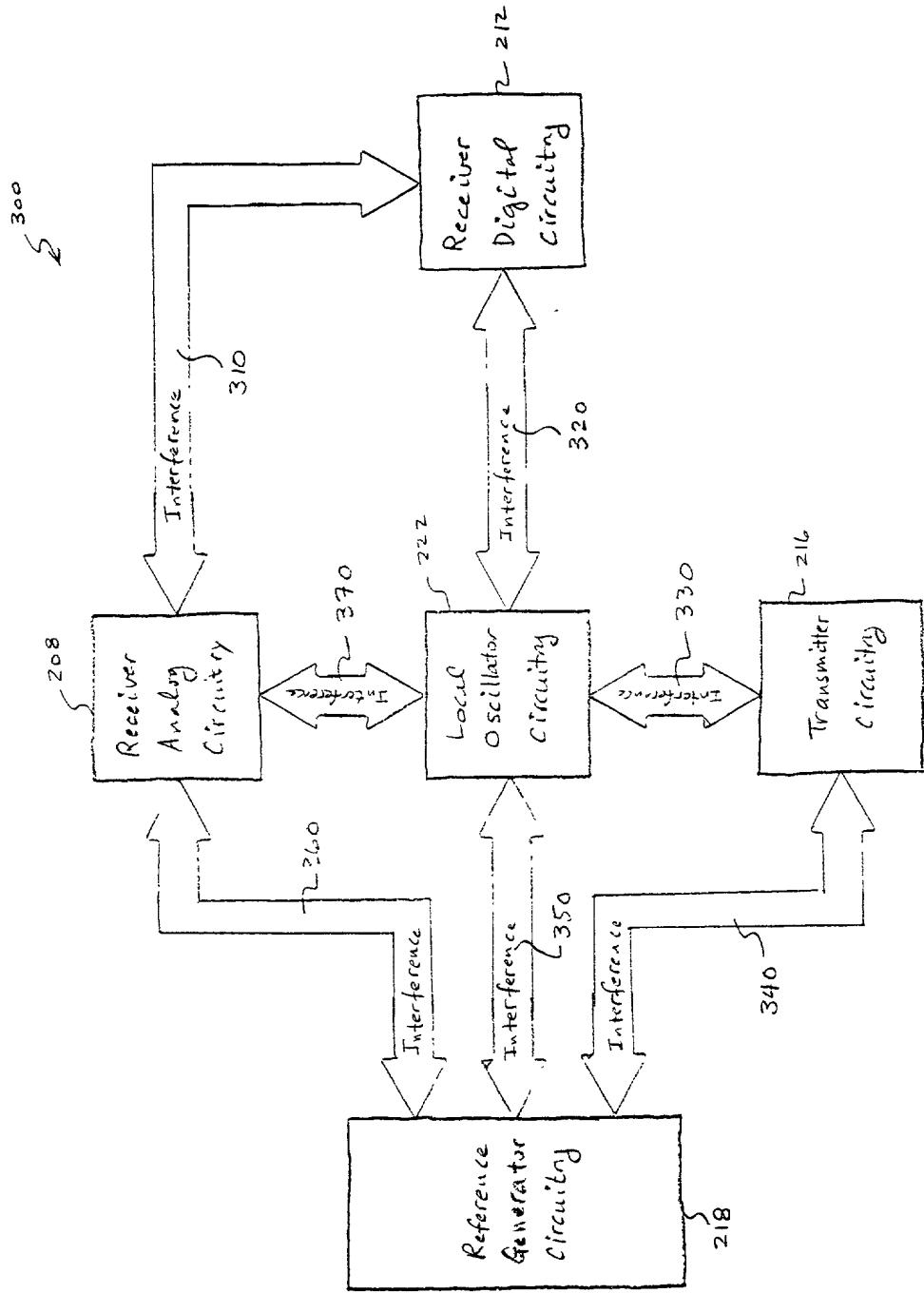


FIG. 3

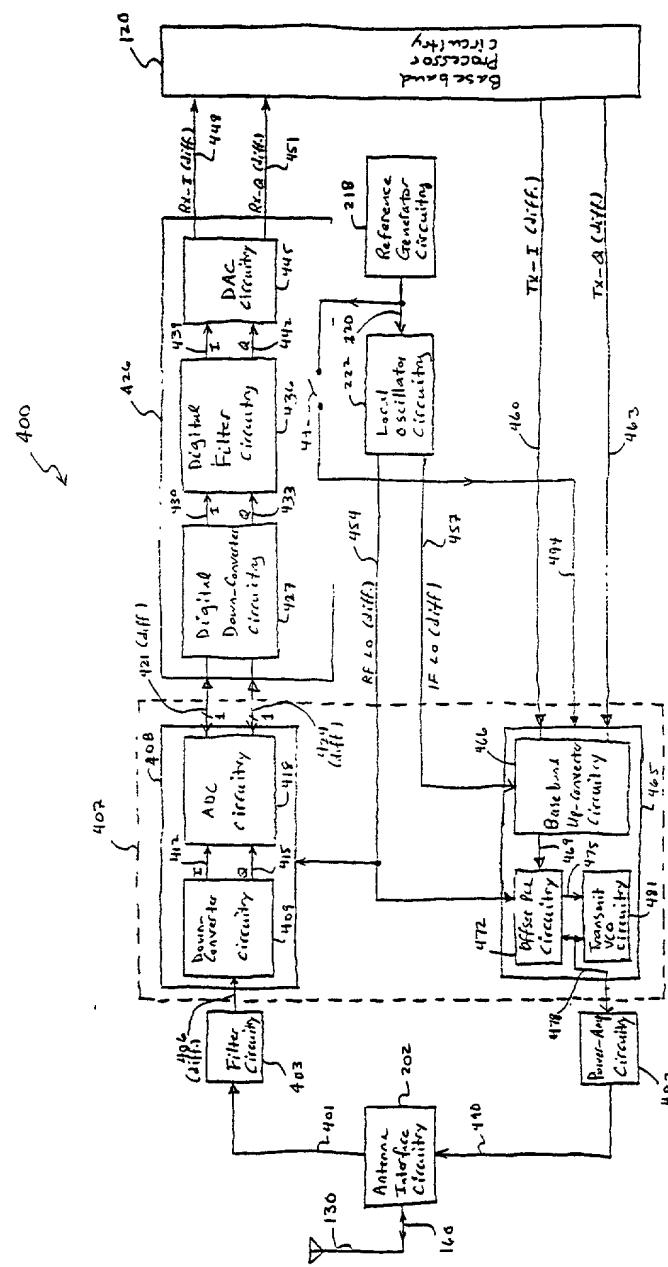


FIG. 4

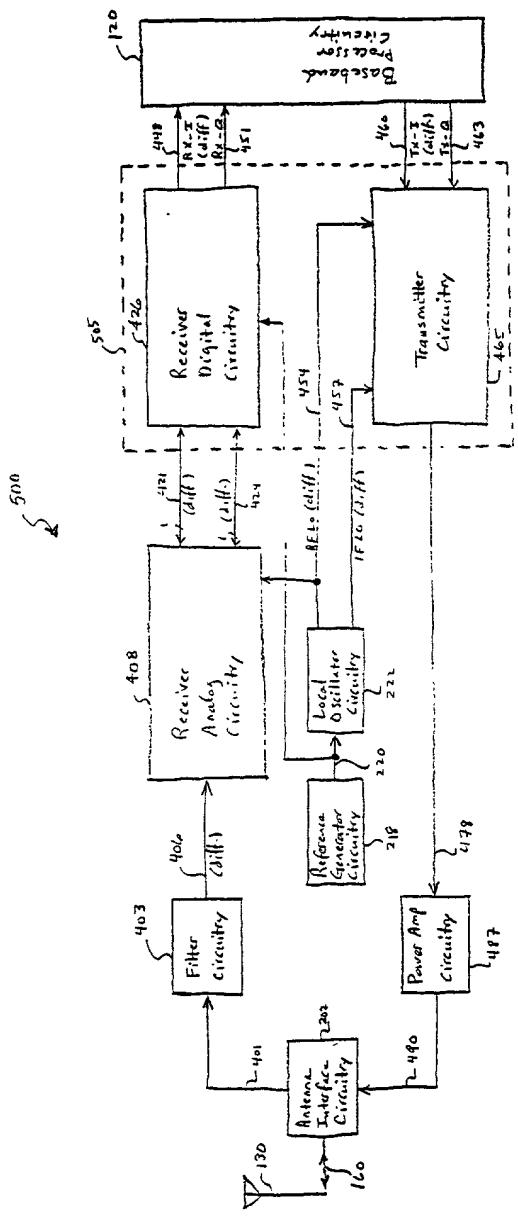


FIG. 5

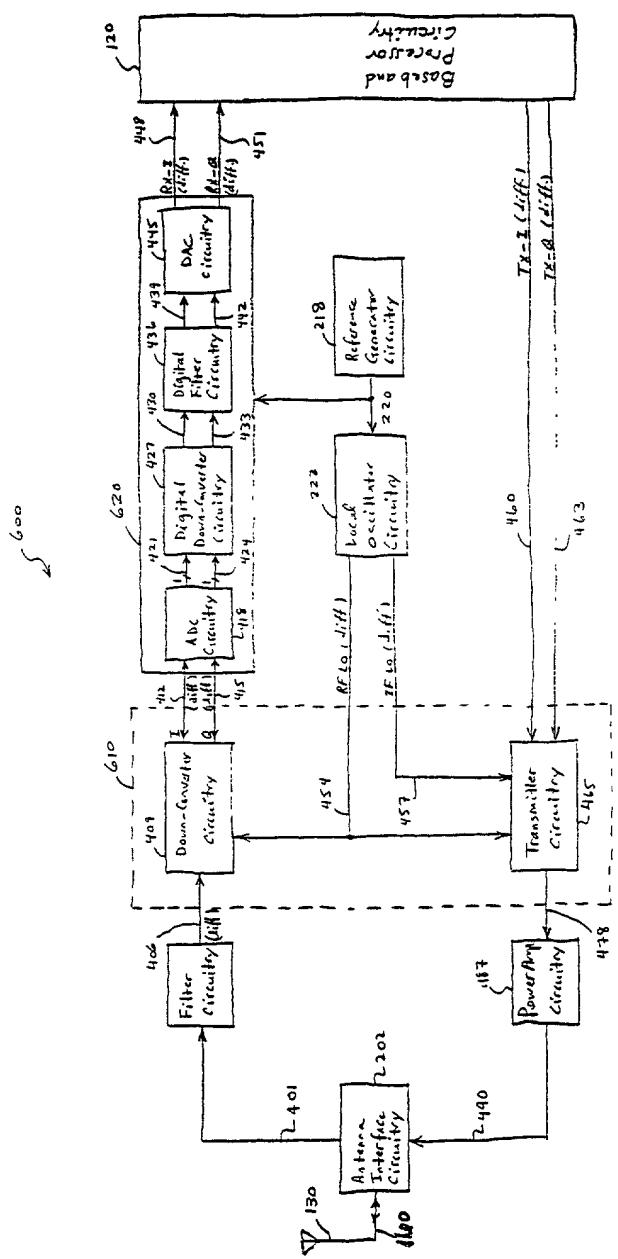


FIG. 6

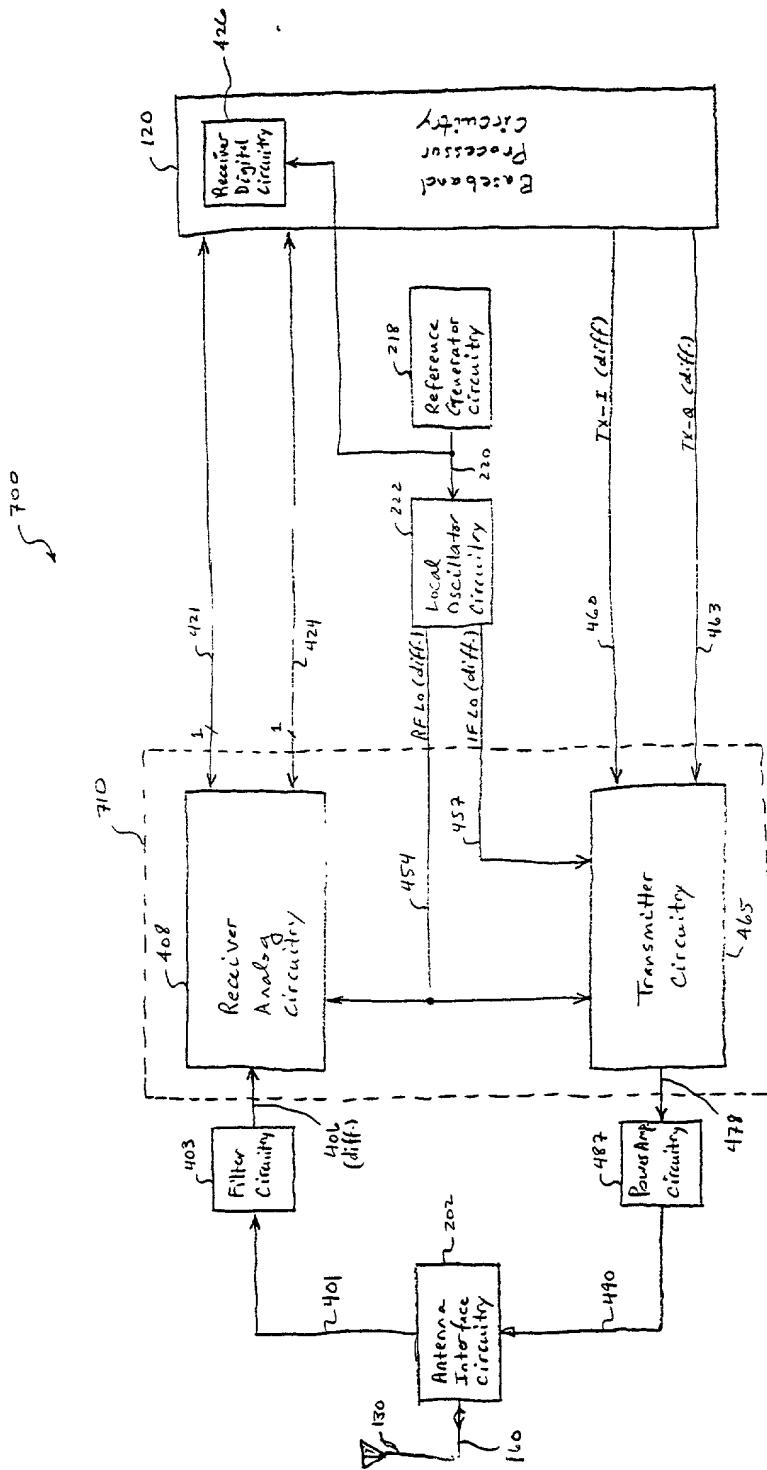
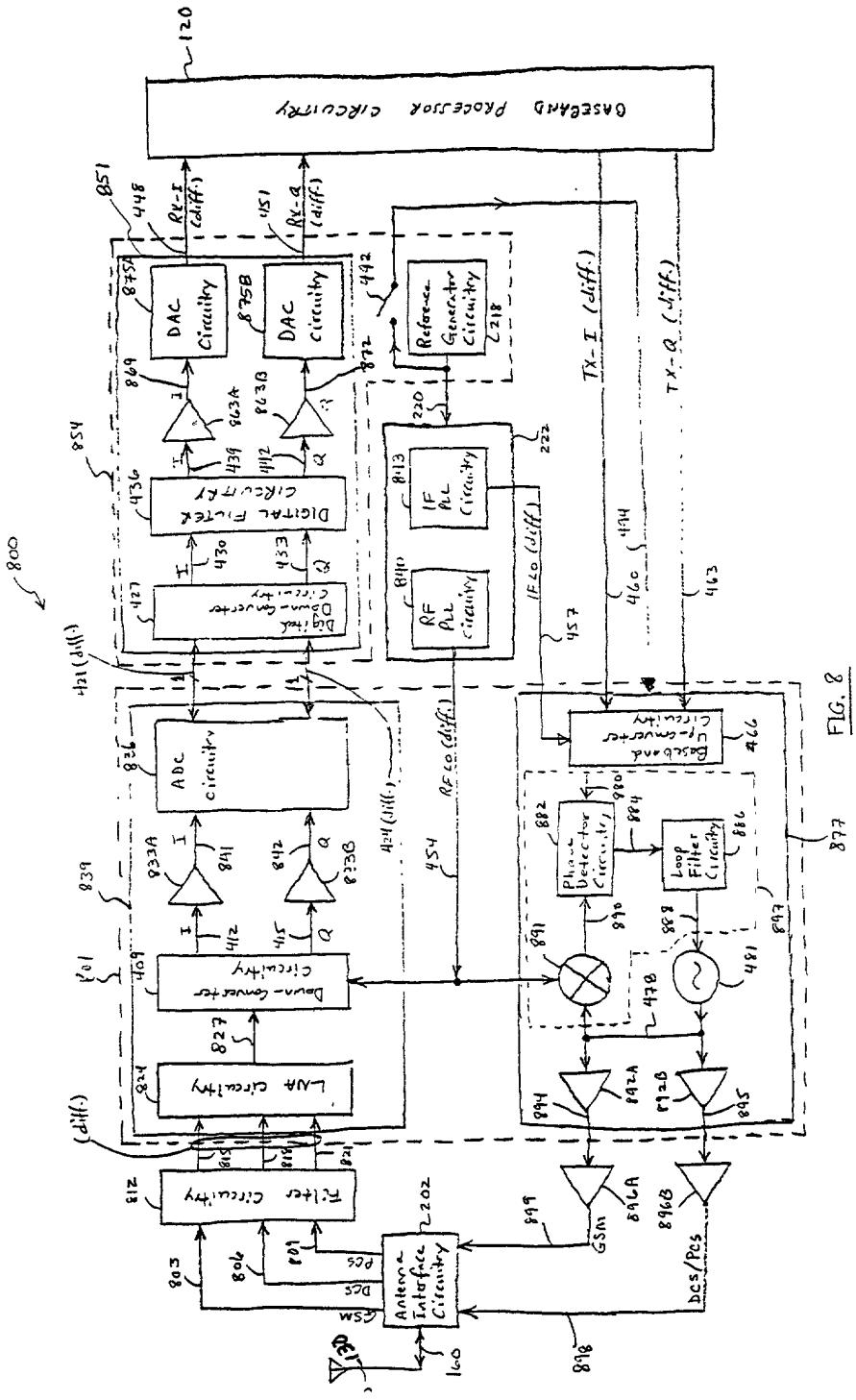


FIG. 4



V105

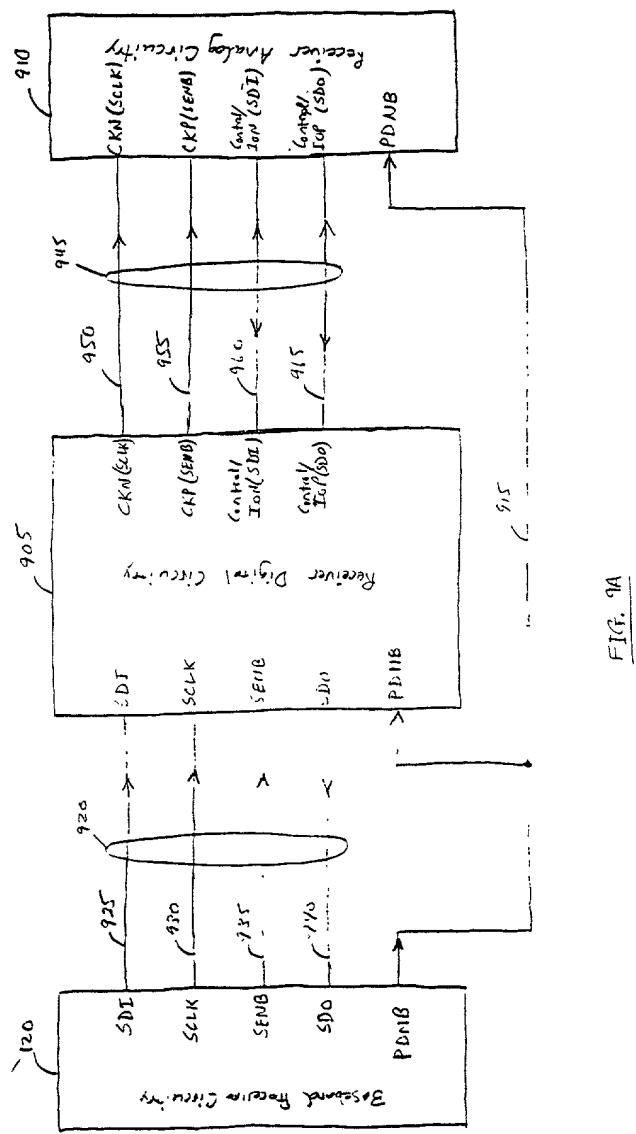
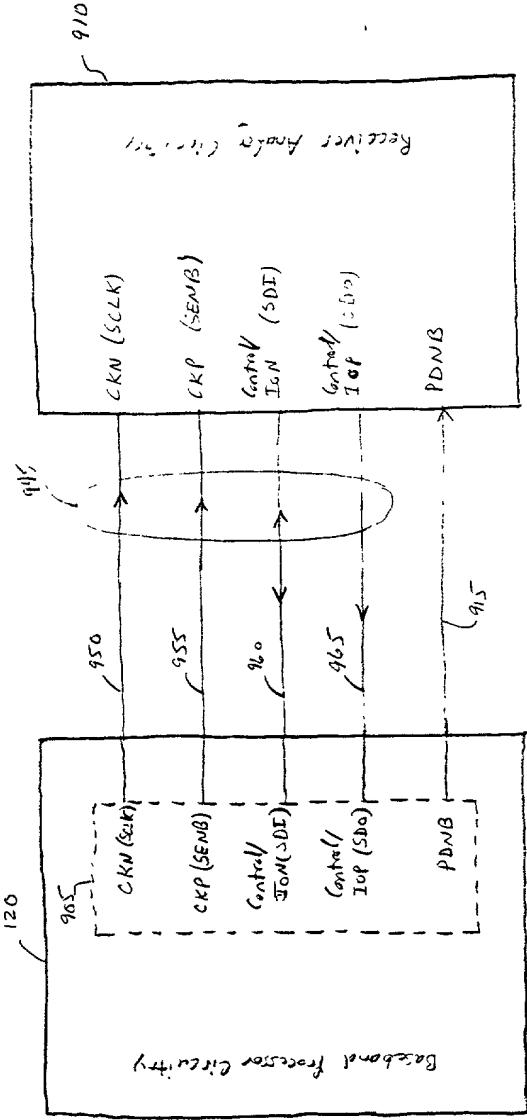


FIG. 9A

FIG. 9B



↙ 900B

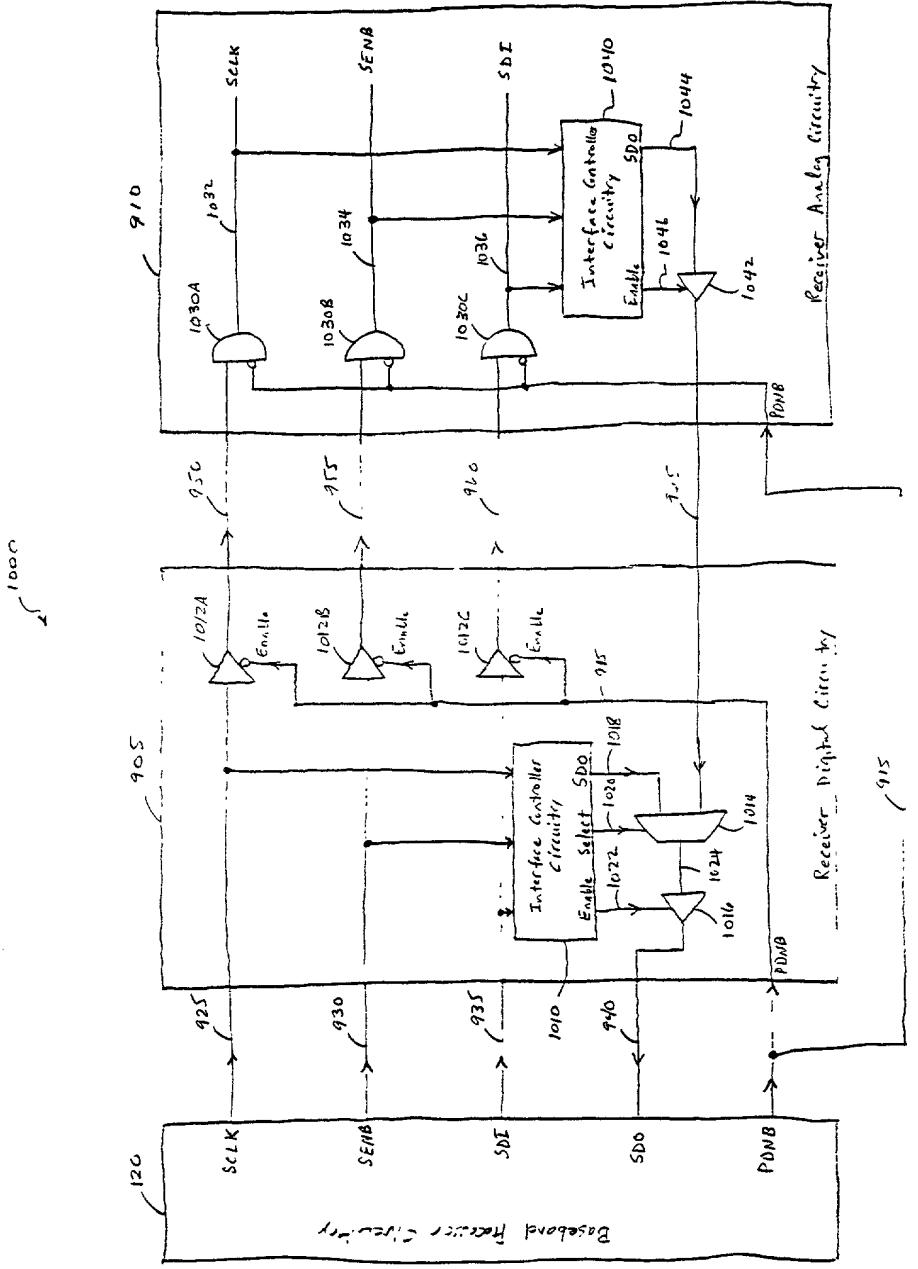


Fig. 16

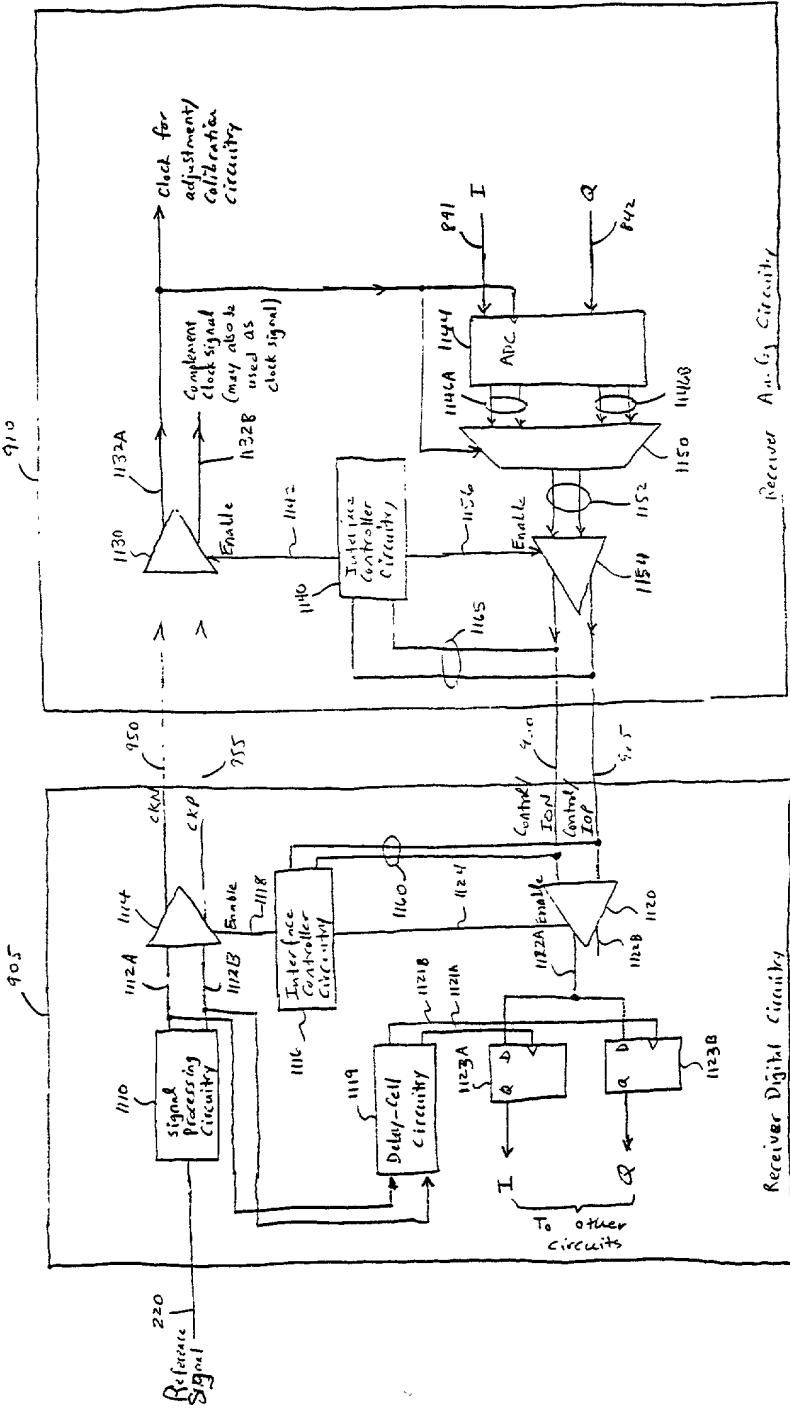


FIG. 11A

11008
↓

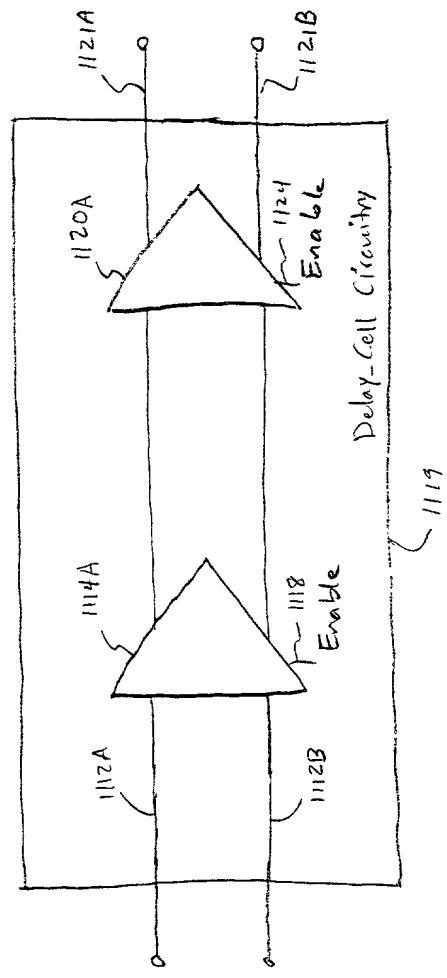


FIG. 11B

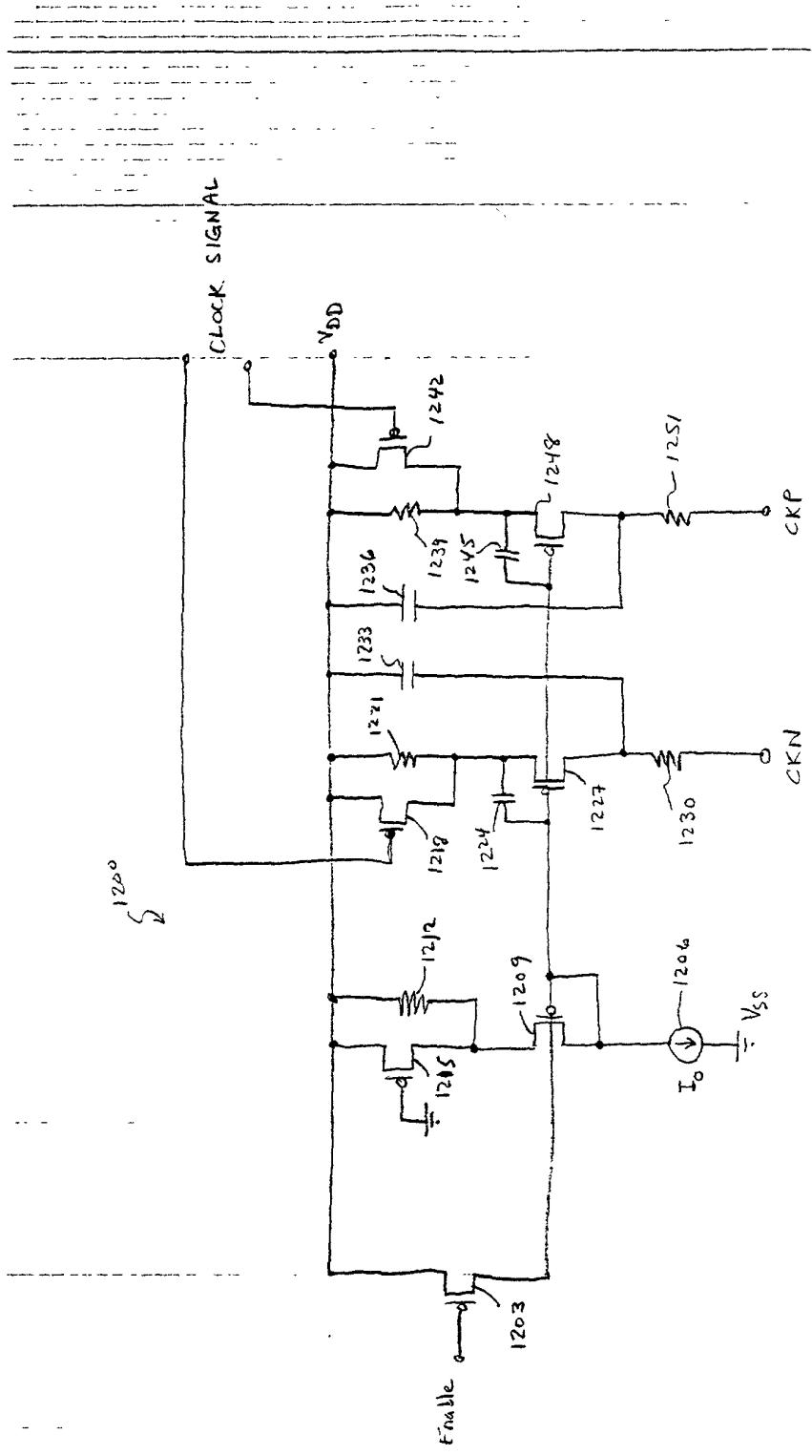
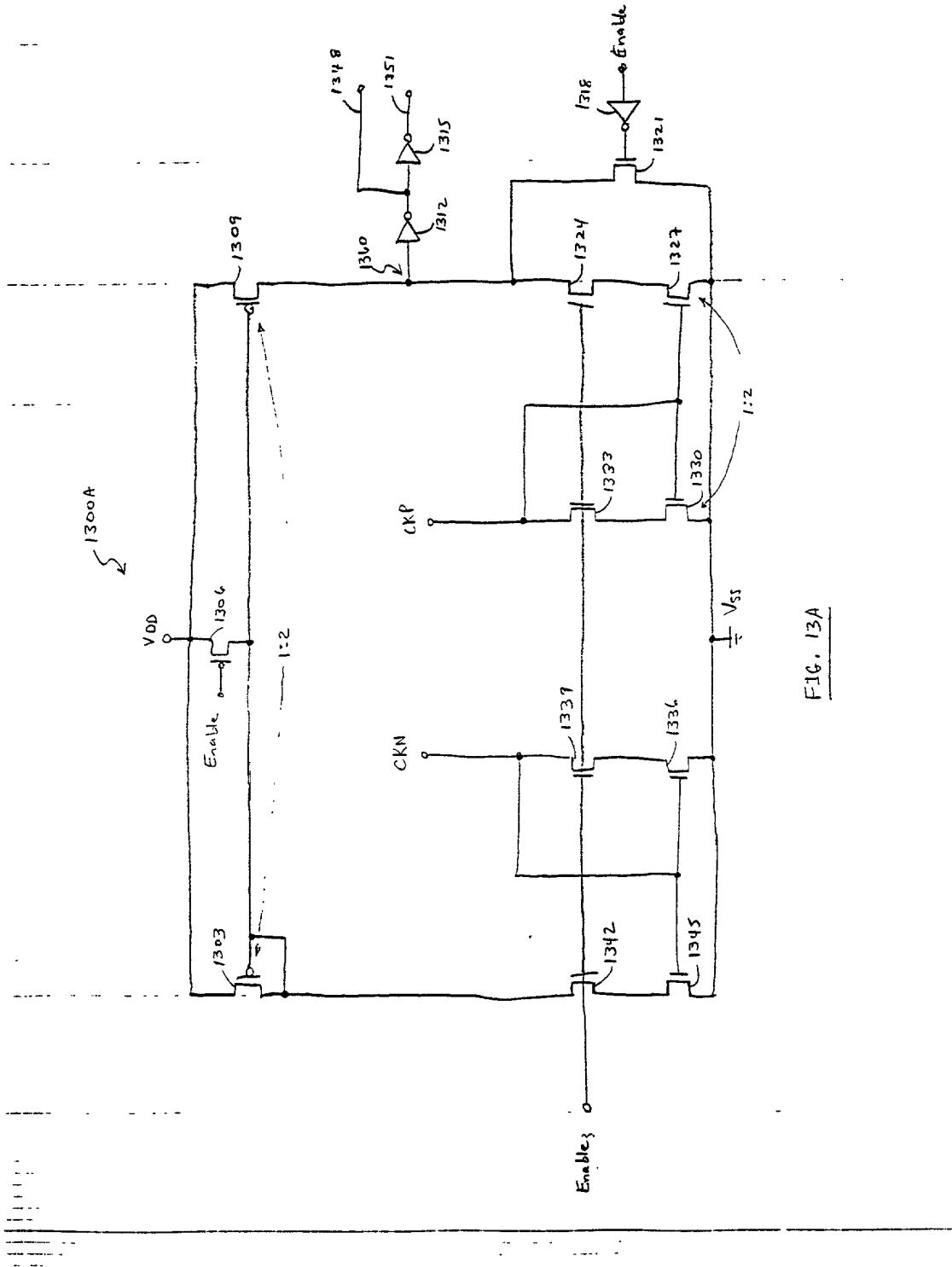


FIG. 12



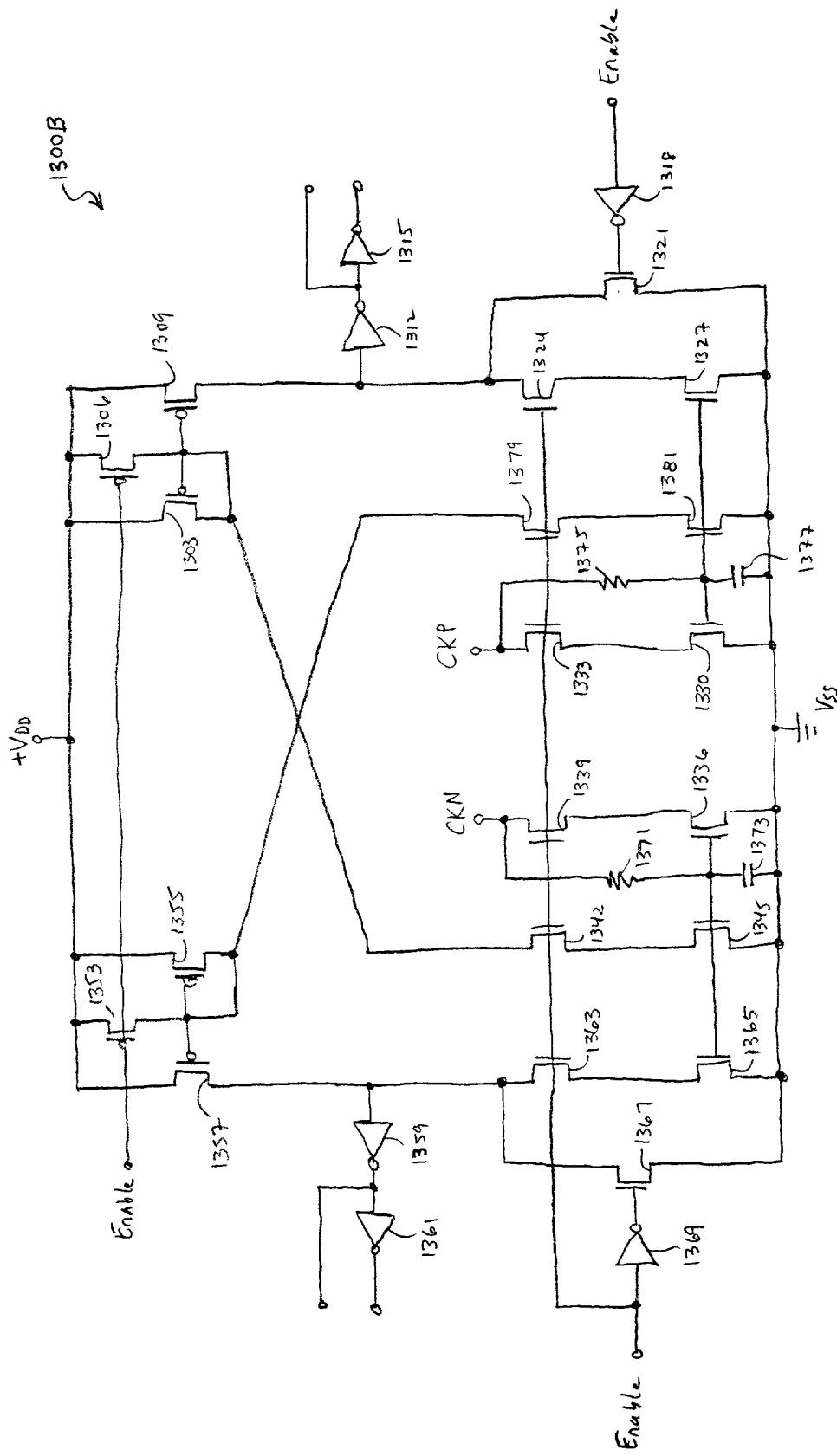
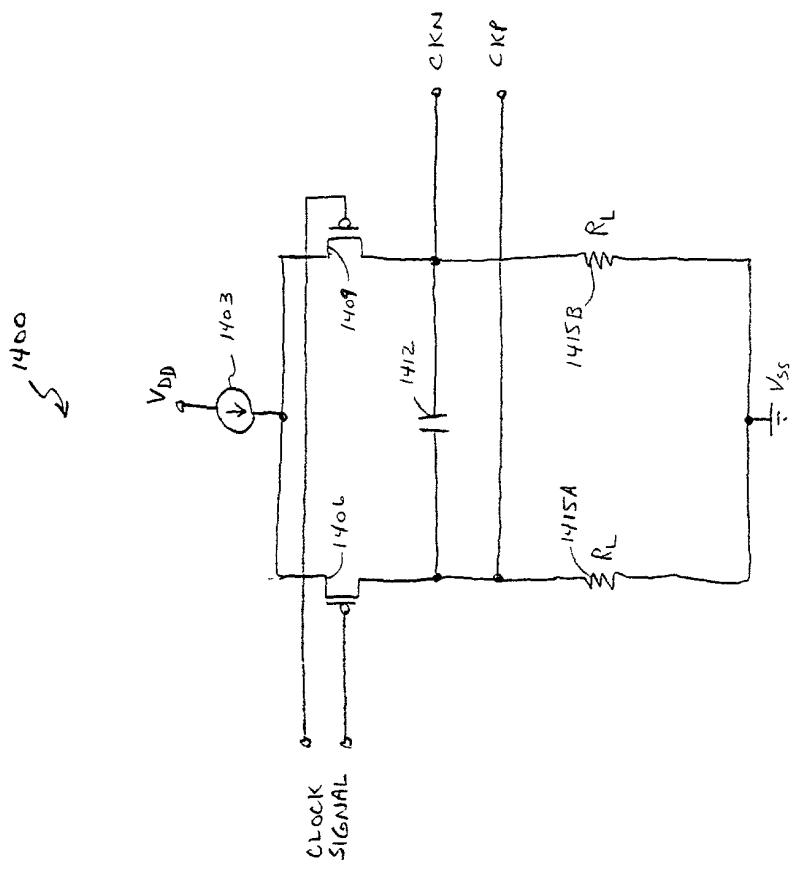


FIG. 13B

FIG. 14



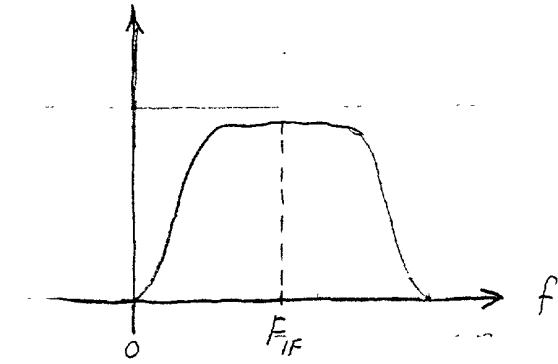


FIG. 15

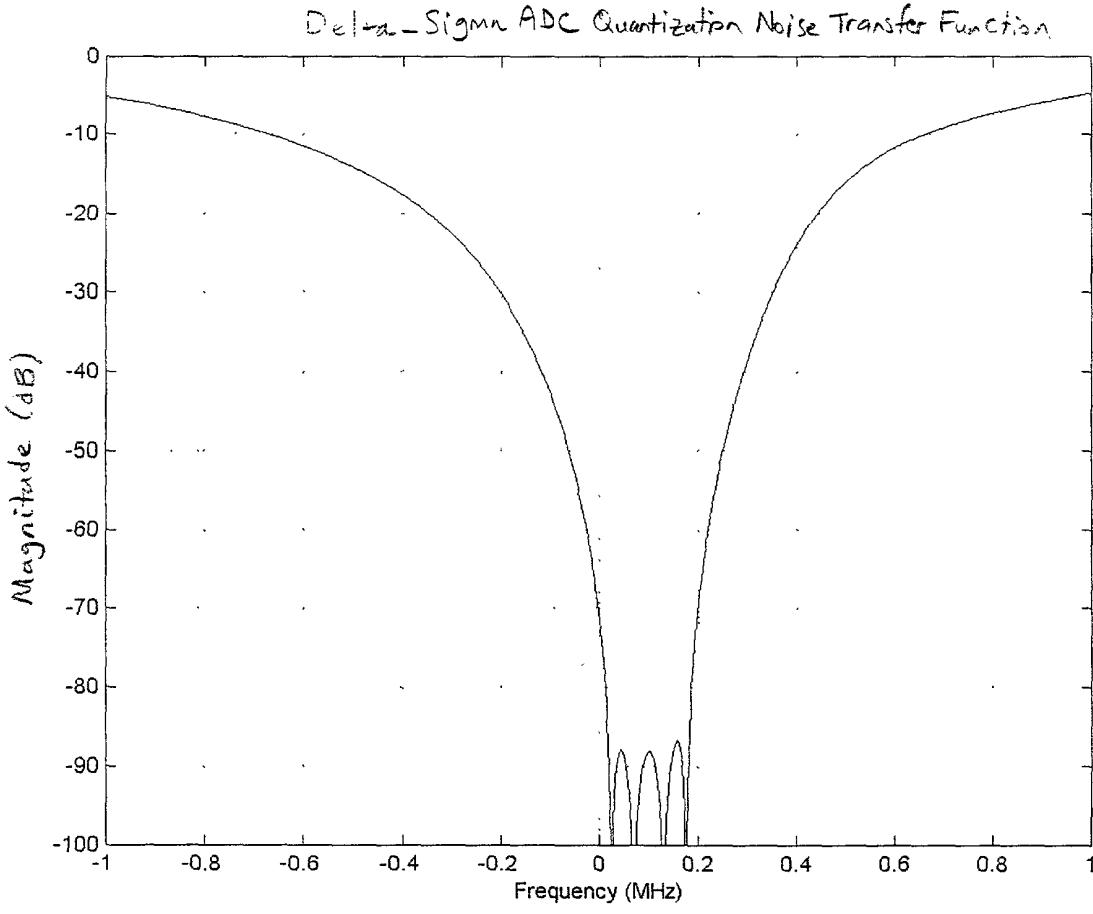


FIG. 16

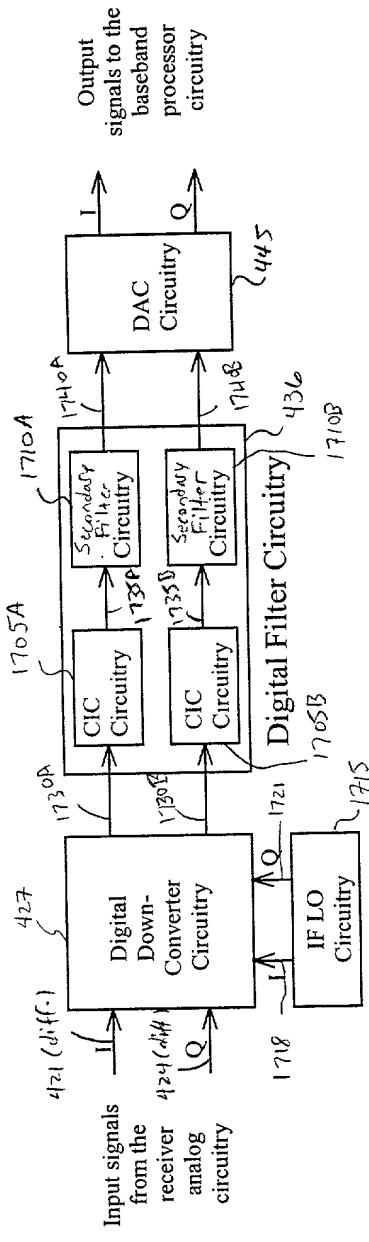


FIG. 17A

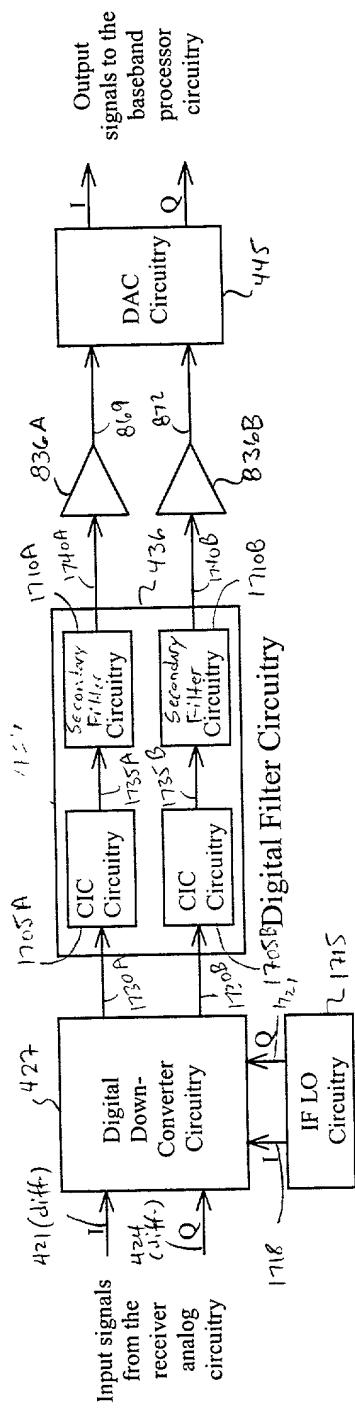


FIG. 17B

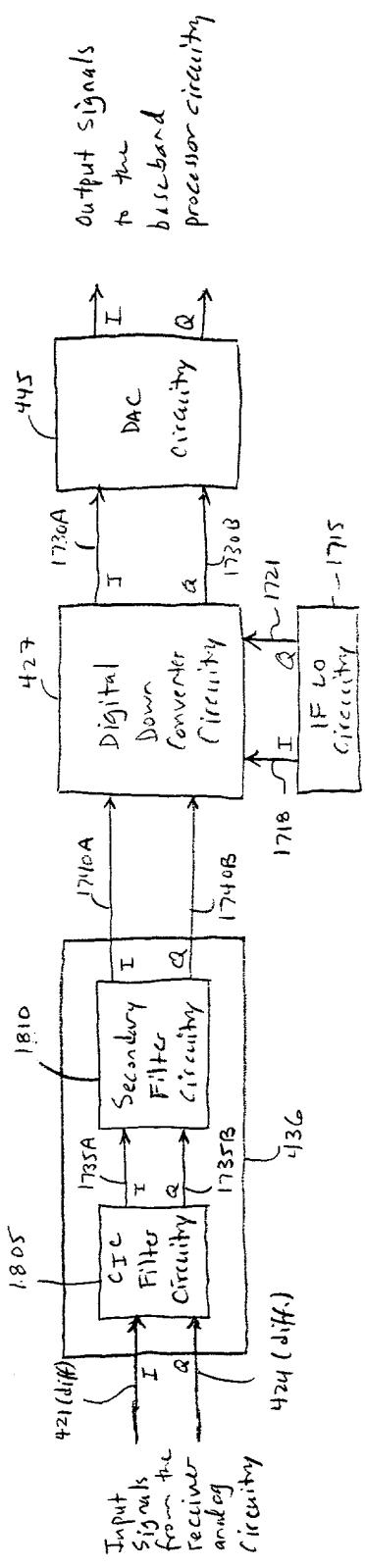


FIG. 18A

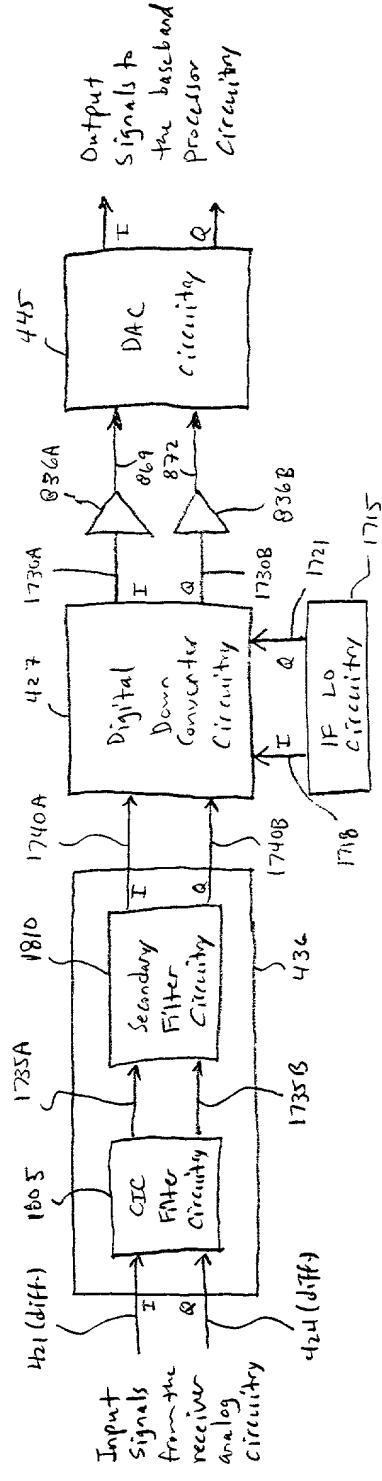


FIG. 18B

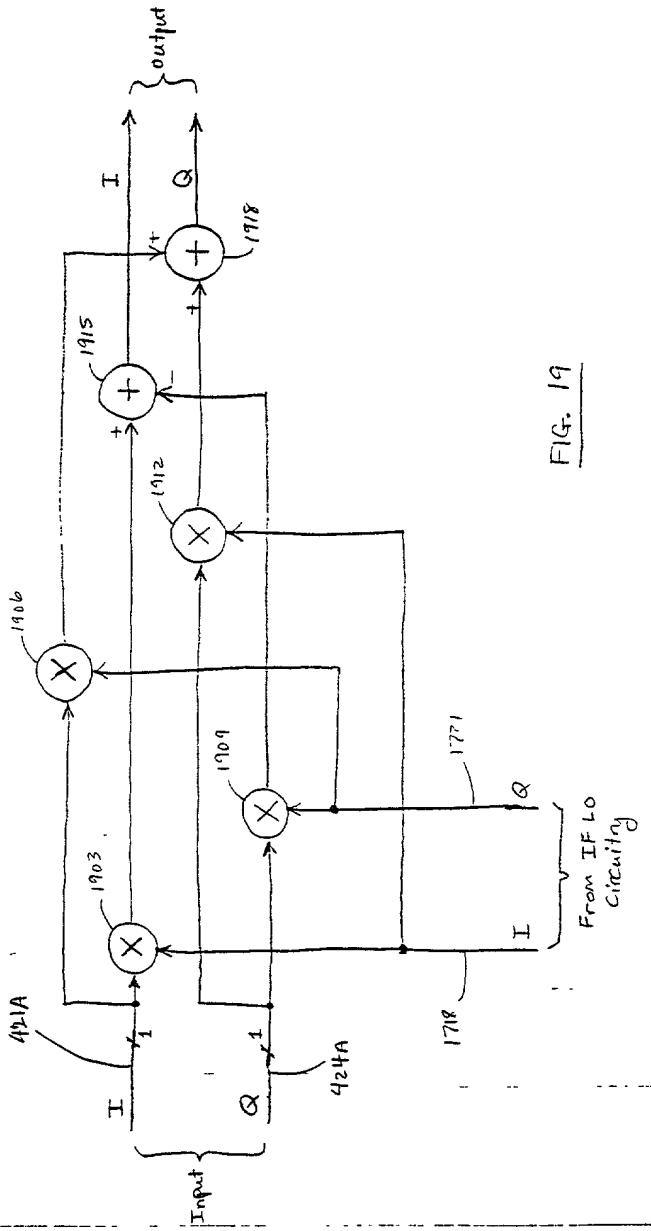


Fig. 19

From IF LO
Circuity

1715

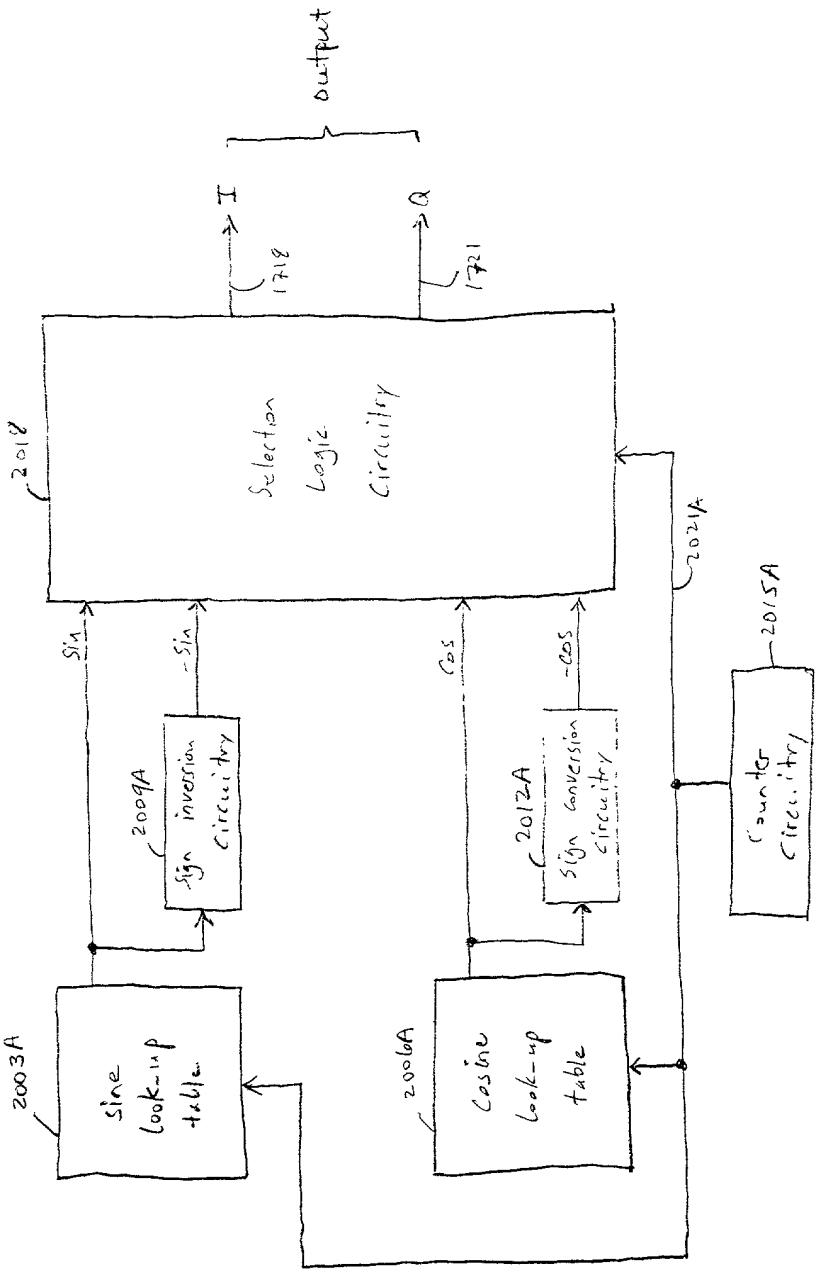


FIG. 20A

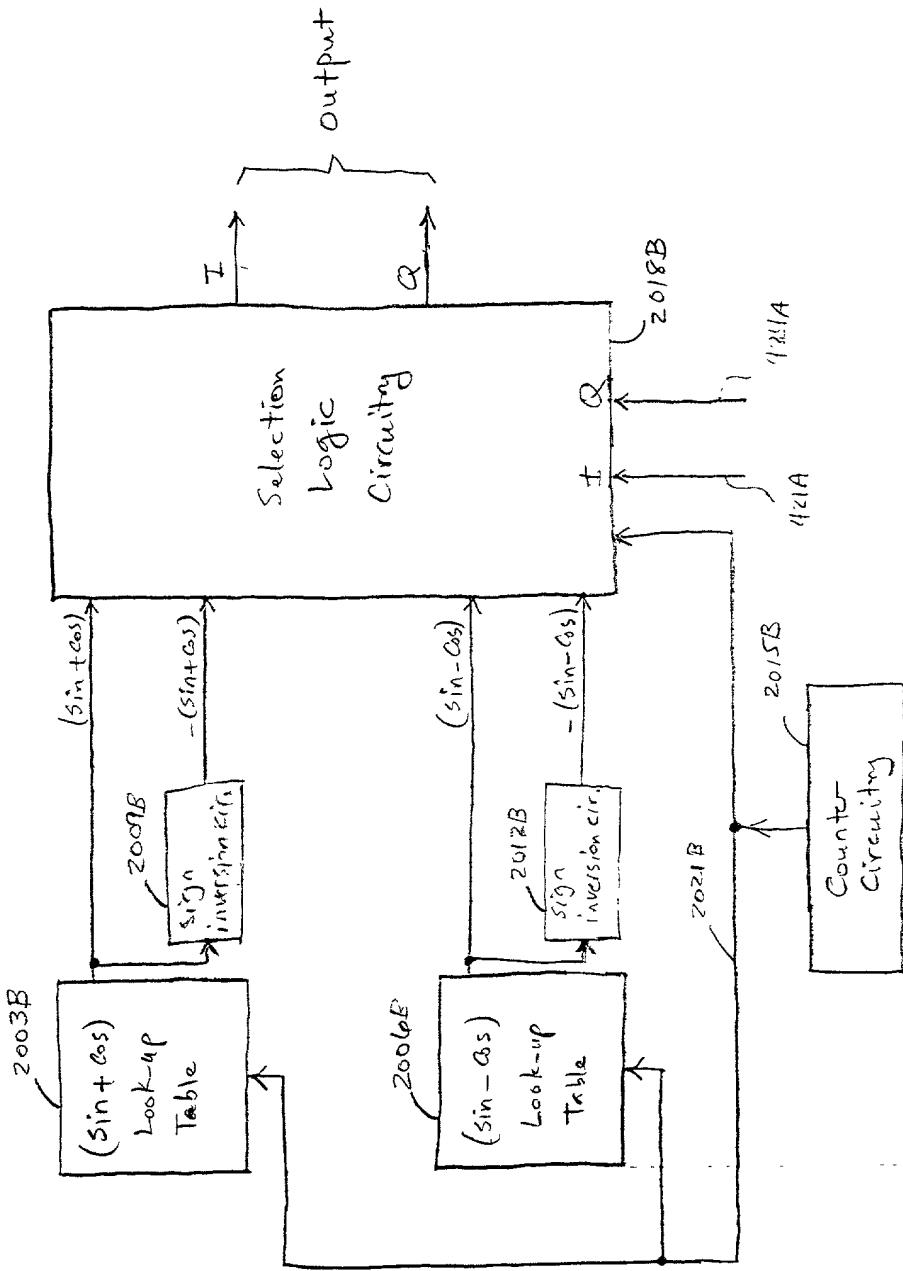


FIG. 20B

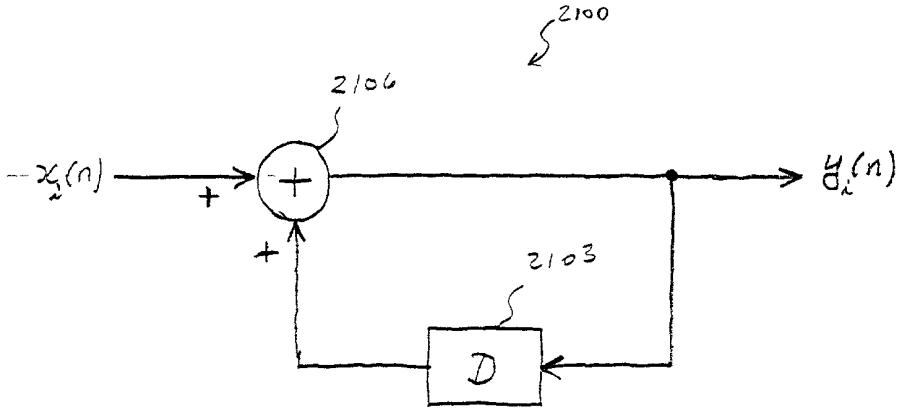


FIG. 21

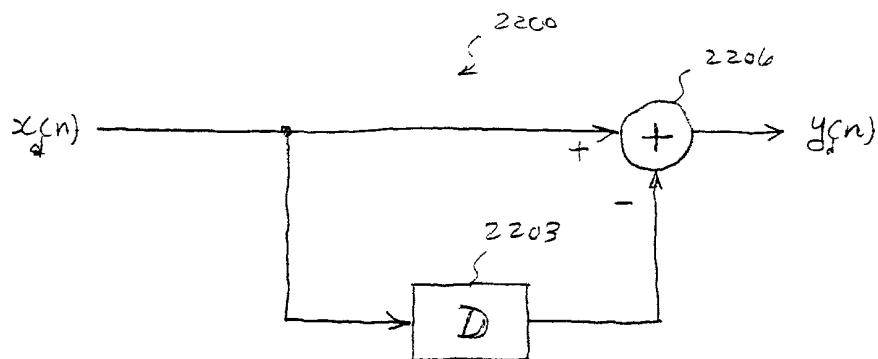


FIG. 22

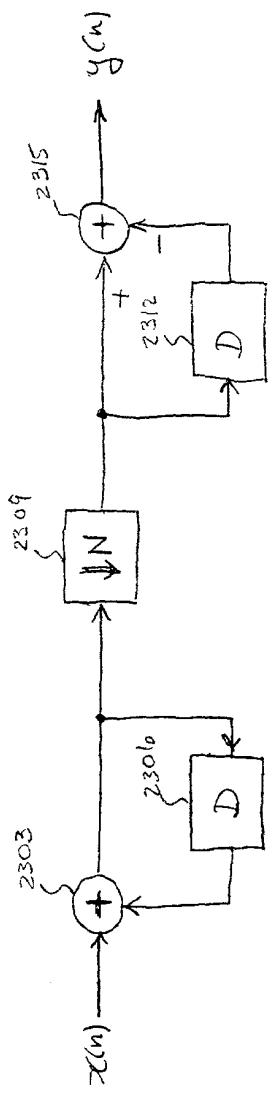


FIG. 23

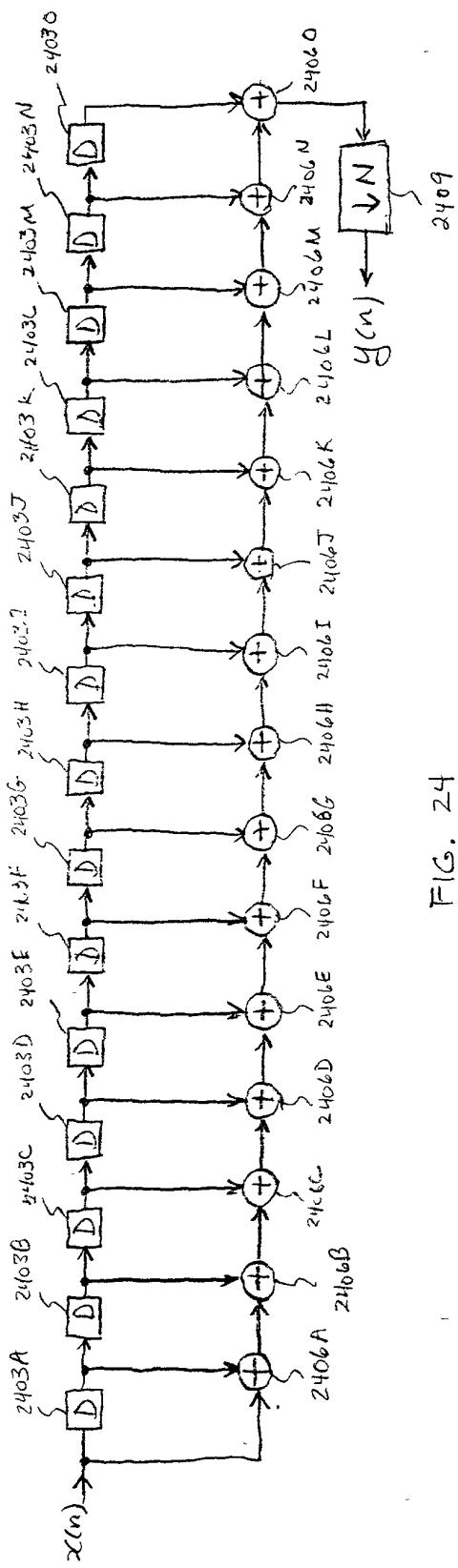


FIG. 24

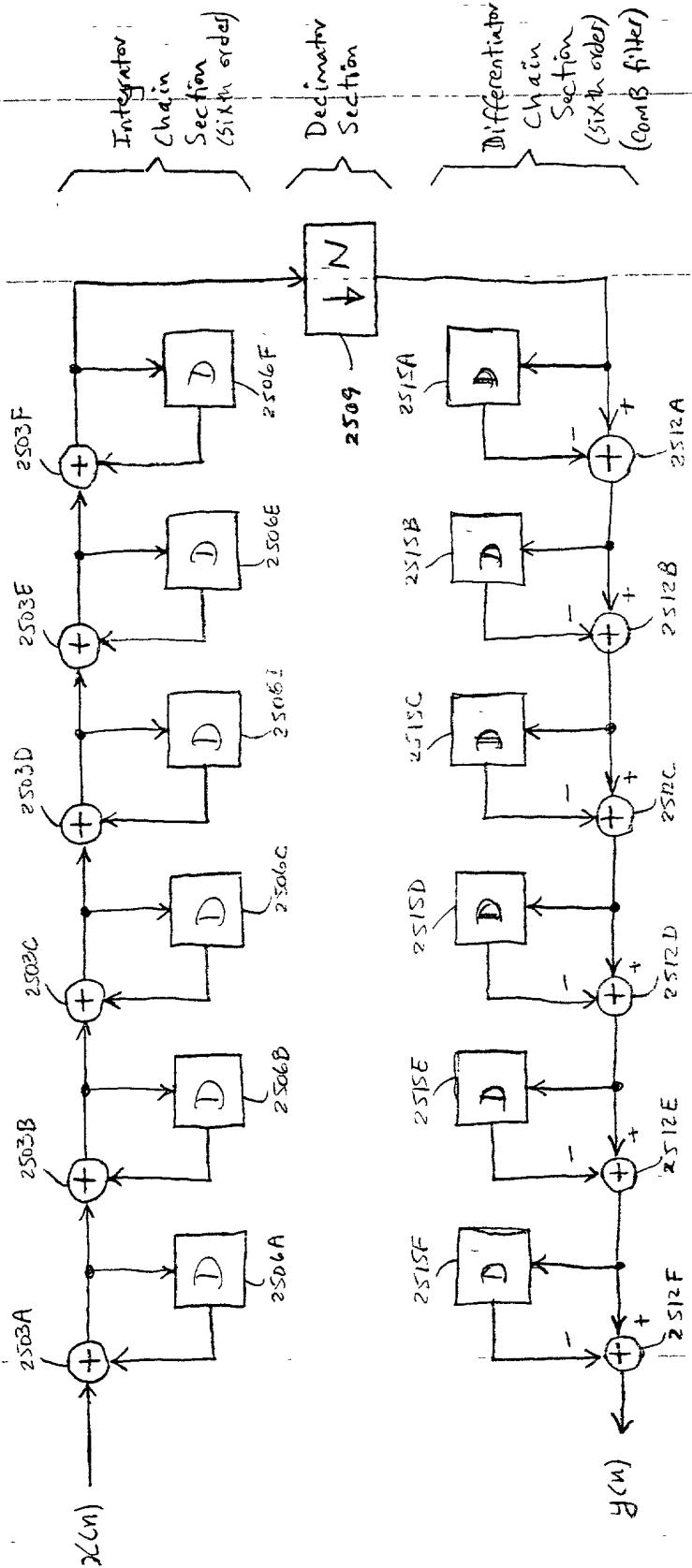


FIG. 25

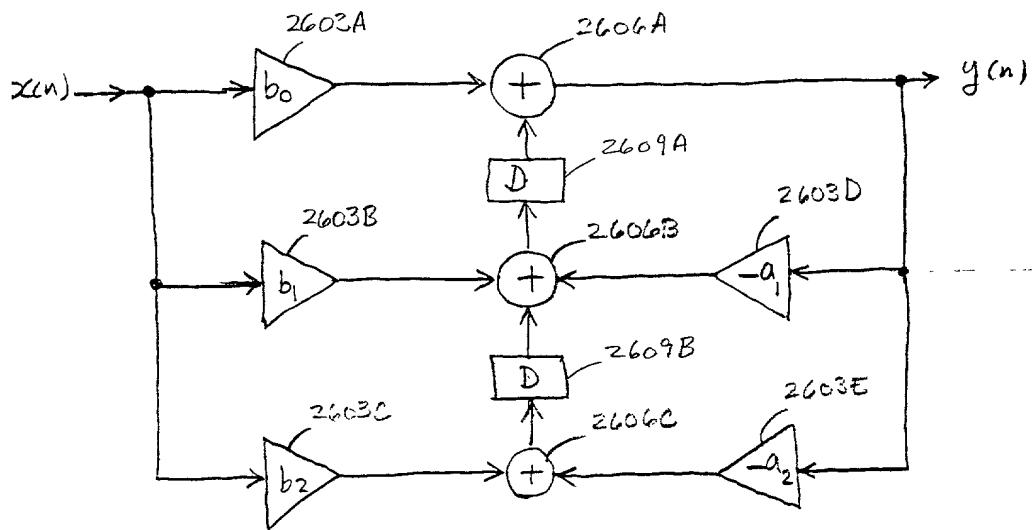


FIG. 26

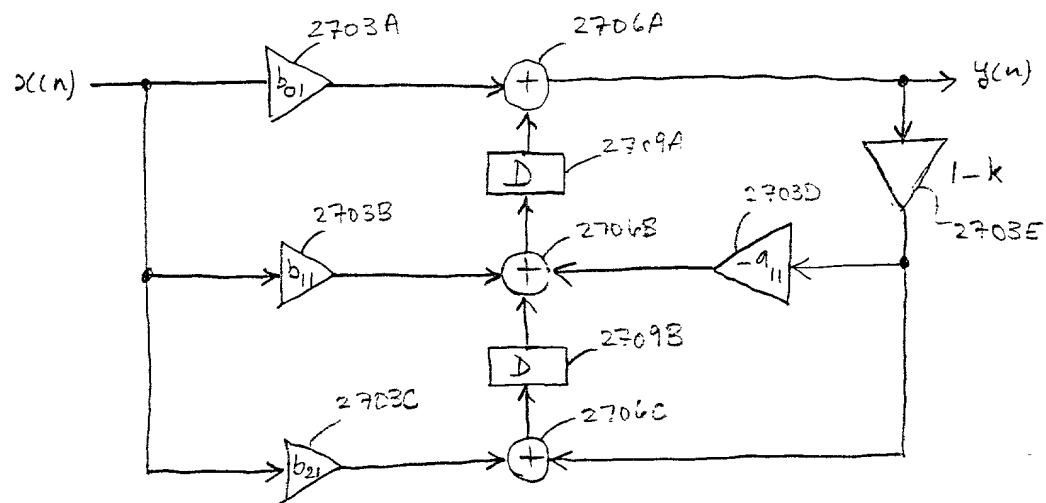


FIG. 27

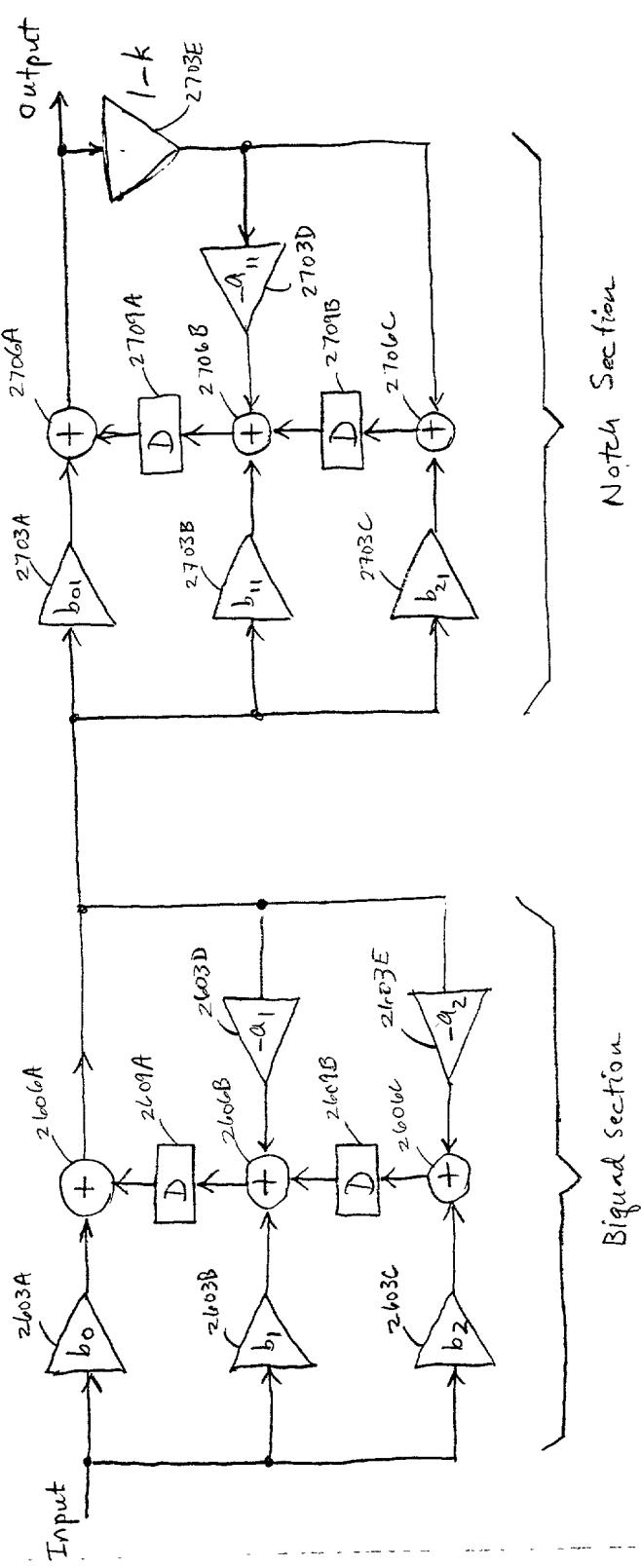


Fig. 28A

Notch Section

Biquad section

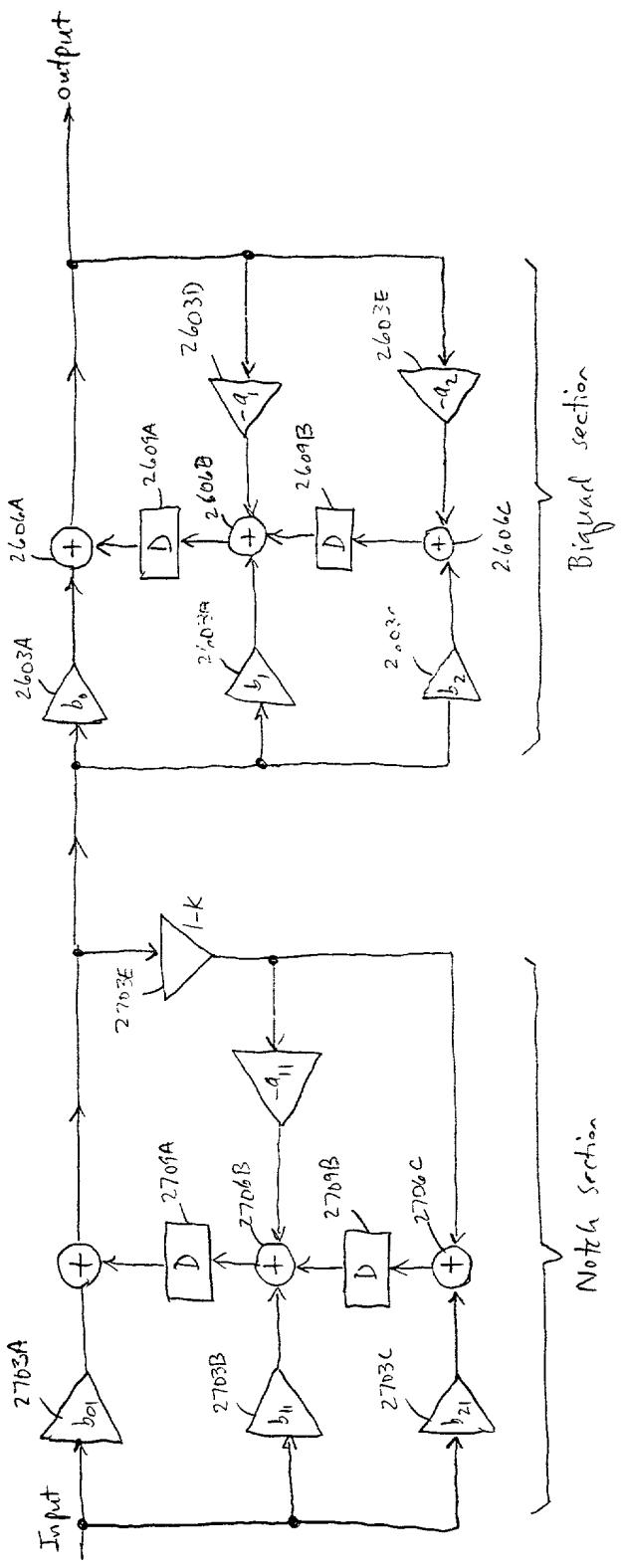


FIG. 29B

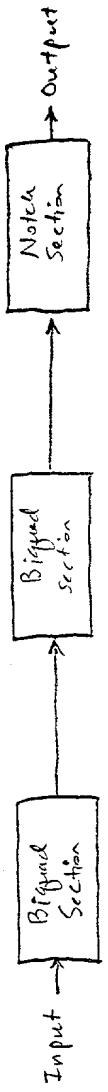


FIG. 29A



FIG. 29B

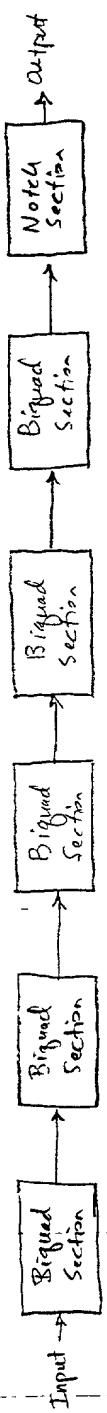


FIG. 29C

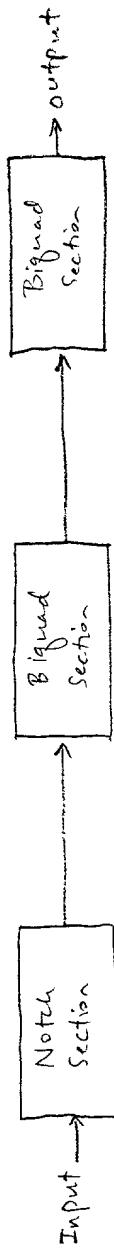


FIG. 29D



FIG. 29E

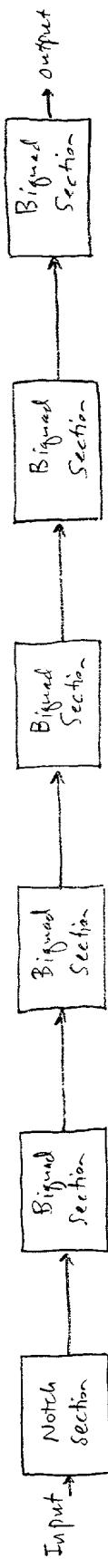


FIG. 29F

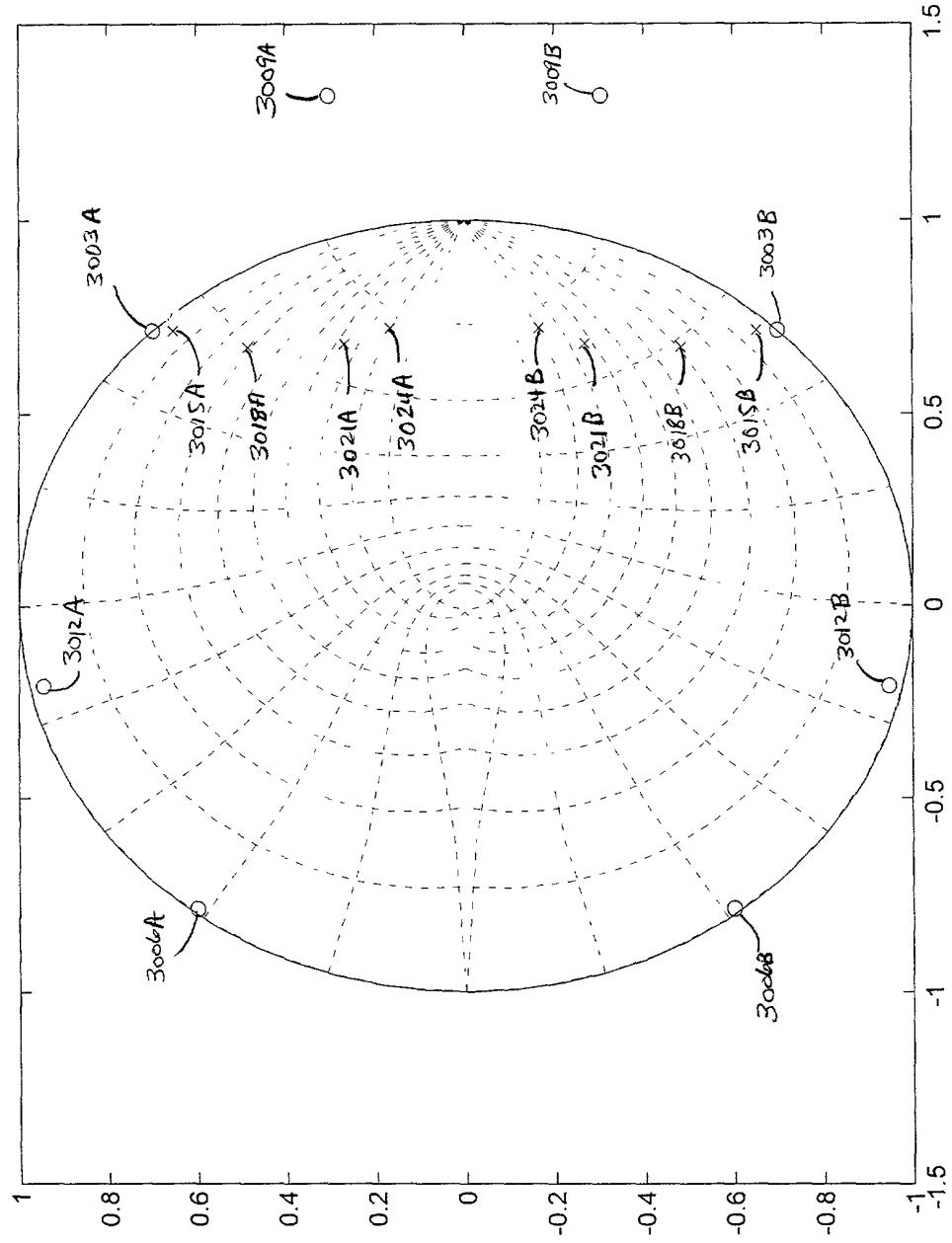


FIG. 30A

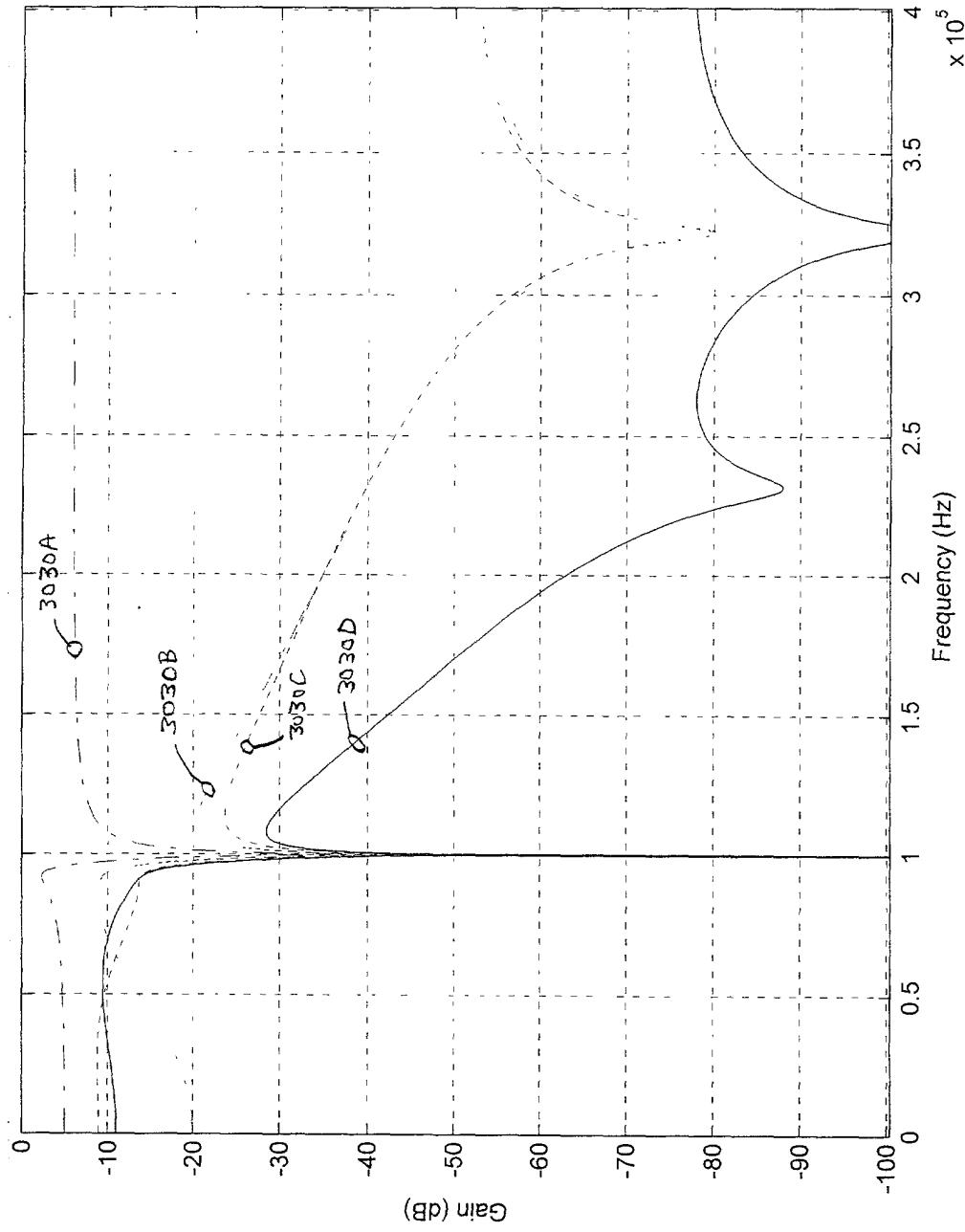


FIG. 30B

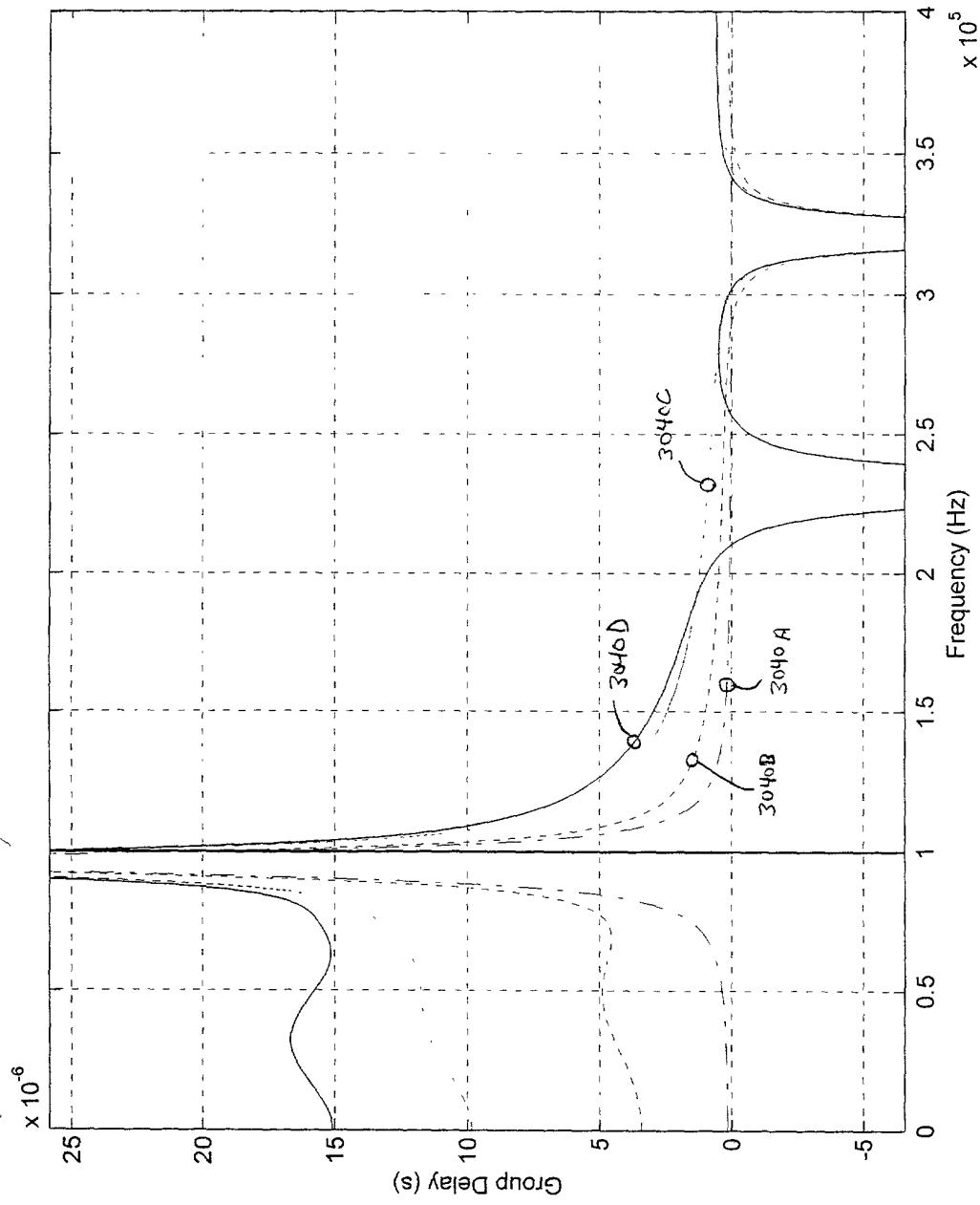


FIG. 30C

FIG. 31

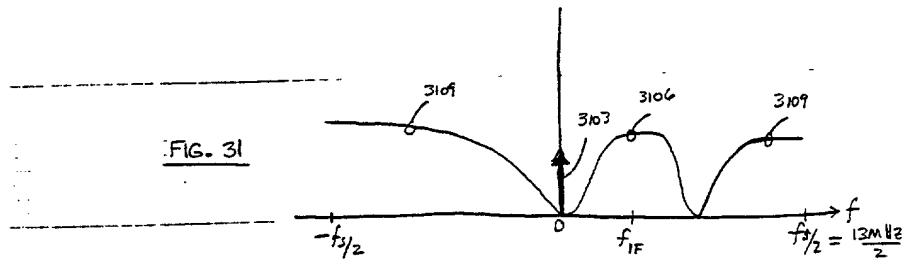


FIG. 32

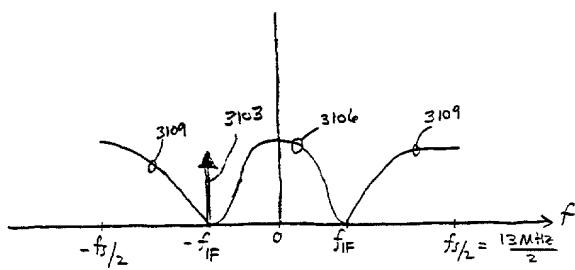


FIG. 33

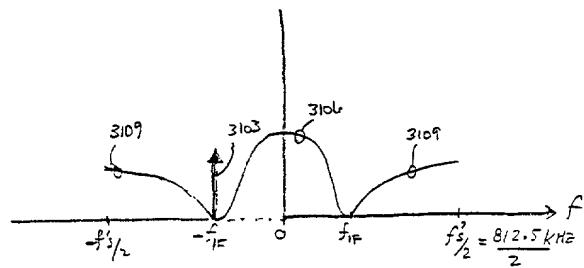


FIG. 34

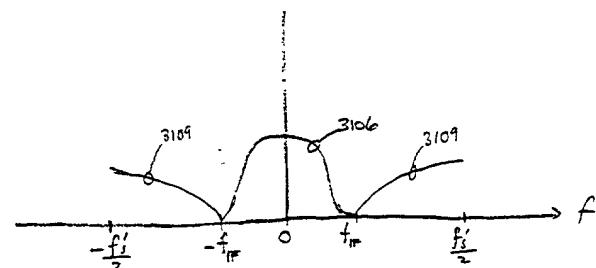
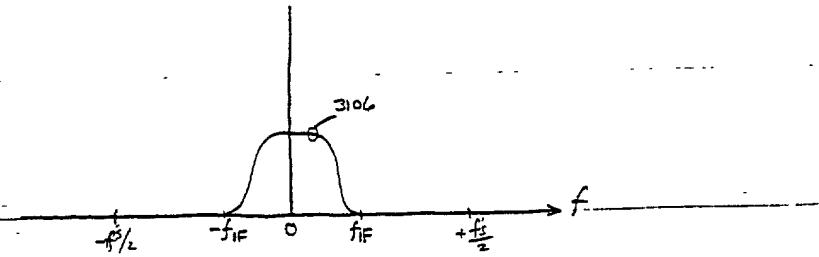
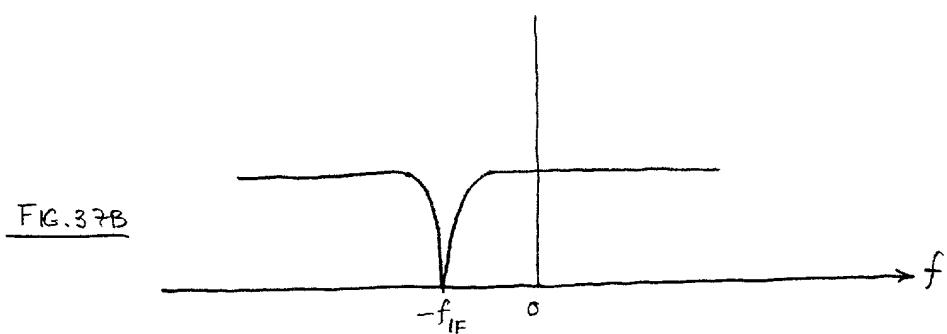
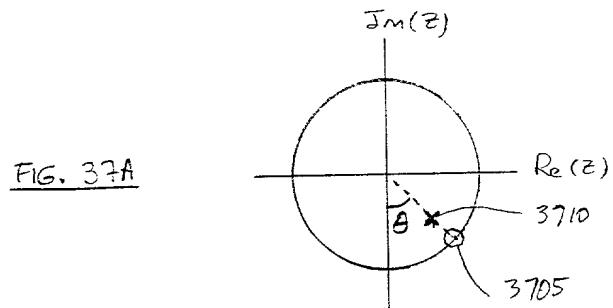
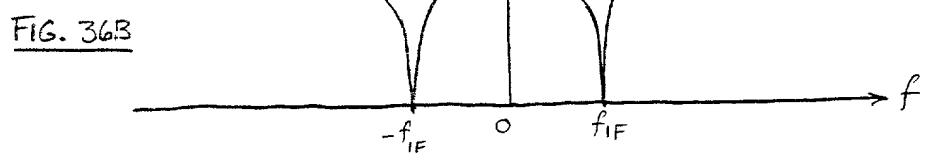
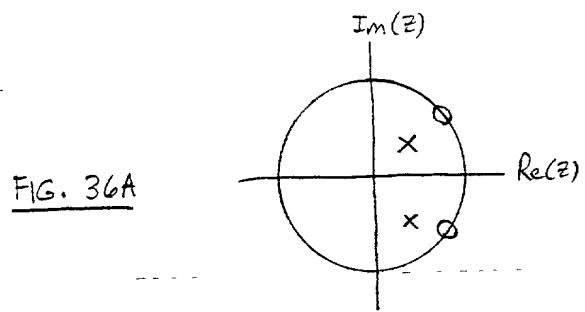


FIG. 35





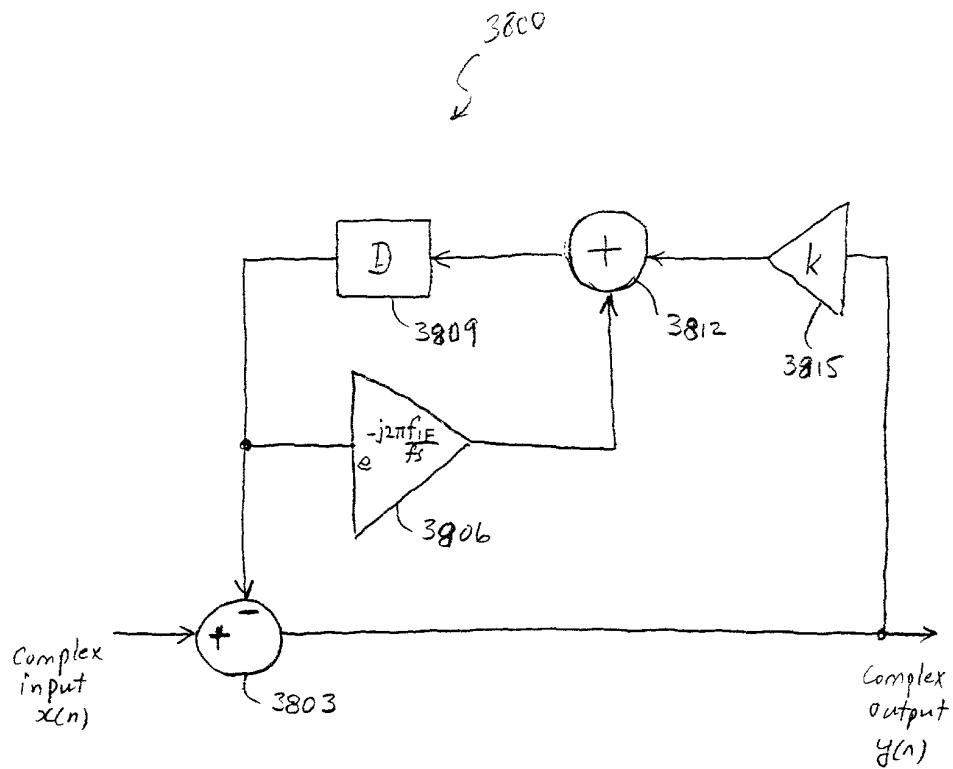


FIG. 38

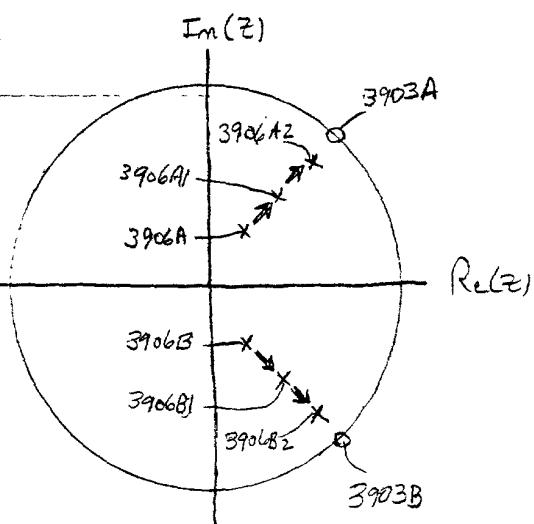


FIG. 39

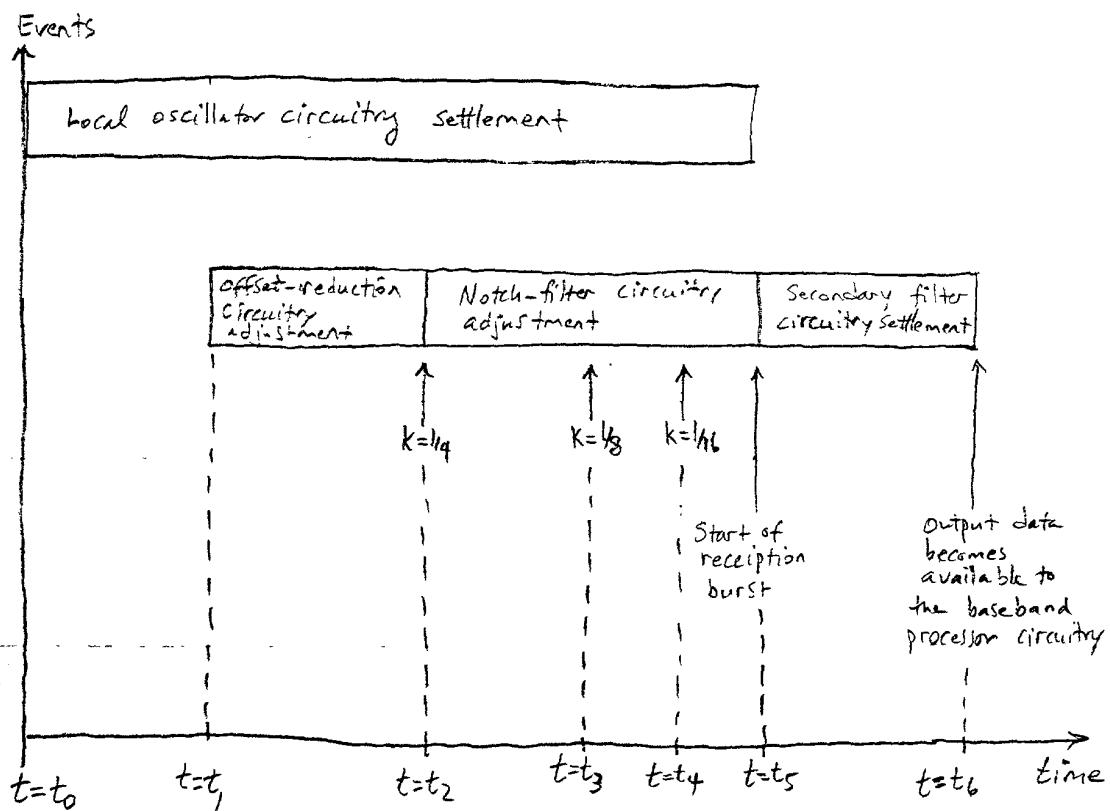


Fig. 40